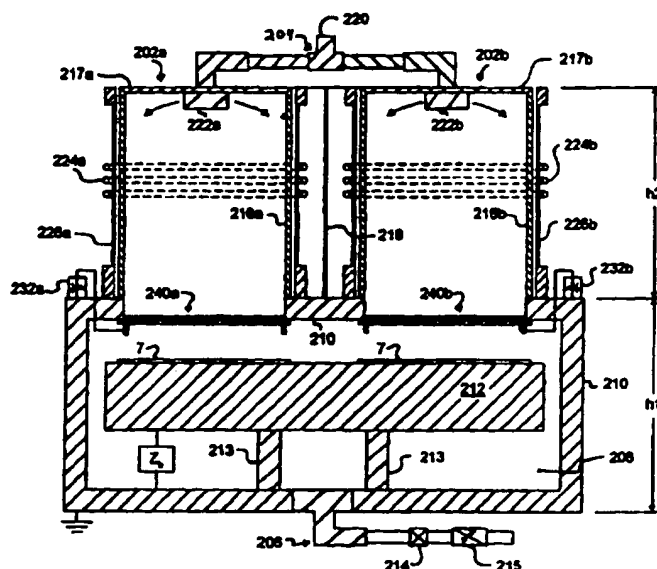




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(54) Title: INDUCTIVE PLASMA REACTOR



(57) Abstract

A plasma reactor and methods for processing semiconductor wafers are described. Gases are introduced into a reactor chamber. An induction coil surrounds the reactor chamber. RF power is applied to the induction coil and is inductively coupled into the reactor chamber causing a plasma to form. A split Faraday shield is interposed between the induction coil and the reactor chamber to substantially block the capacitive coupling of energy into the reactor chamber which may modulate the plasma potential. The configuration of the split Faraday shield may be selected to control the level of modulation of the plasma potential. For etch processes, a separate powered electrode may be used to accelerate ions toward a wafer surface. For isotropic etching processes, charged particles may be filtered from the gas flow, while a neutral activated species passes unimpeded to a wafer surface.

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INDUCTIVE PLASMA REACTOR

BACKGROUND OF THE INVENTION

1. Field of the Invention

The field of the present invention relates in general to plasma reactors and processes typically used for wafer processing or the like. More particularly, the field of the invention relates to a wafer processing plasma reactor in which the plasma is generated primarily by inductively coupled power.

2. Background

A variety of approaches are currently in use for deposition, etching, resist removal, and related processing for semiconductor wafers and other substrates. A typical process may involve the placement of photoresist, the etching of areas unprotected by the resist, and the removal of the residual resist. One method for etching and resist removal involves the immersion of the substrate in a chemical bath that attacks the substrate or the resist. This method is generally known as wet chemical etching or stripping. While wet etching is conceptually straightforward, it has significant disadvantages. For instance, wet etching requires the storage, handling, and disposal of toxic chemicals. In addition, wet chemical etch is isotropic, that is, it occurs in all directions relatively equally. This leads to undesired lateral etching under the photoresist, called undercutting, which limits the dimensional control that can be achieved with wet etch.

An alternative processing technology involves the use of a plasma for deposition, etching, resist removal, or the like. A plasma is a nearly electrically neutral ionized gas. It contains a substantial density of free electrons and positively charged ions. To remain ionized, a gas must constantly receive energy to offset the recombination of charged particles which

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occurs mostly on the walls of the reactor chamber. In conventional anisotropic plasma processing, the pressure must also be kept low to reduce the collision rate of the ions. Plasmas for etching are typically formed by applying a radio-
5 frequency (RF) electric field to a gas held at low pressure in a vacuum chamber.

Anisotropic plasma etching in the fabrication of integrated circuit devices is desirable because it can much better control feature dimensions and sidewall profiles by
10 processing under conditions far from thermodynamic equilibrium. This enables the production of integrated circuit features having sidewalls with a precisely defined location that extend substantially vertically from the edges of the masking layer. This is important in devices that have a feature size and
15 spacing comparable to the depth of the etch.

In Figure 1 is shown a typical wafer processing plasma reactor used for etching. This reactor includes a metal wall 1 that encloses a plasma reactor chamber 2. Wall 1 is grounded and functions as one of the plasma electrodes. Gases are
20 supplied to chamber 2 from a gas source 3 and are exhausted by an exhaust system 4 that actively pumps gases out of the reactor to maintain a low pressure suitable for a plasma process. An RF power supply 5 is connected to a second, powered electrode 6 that capacitively couples power into the
25 chamber 2 to form a plasma. A wafer 7 is positioned on or near powered electrode 6 for processing. Wafers are transferred into and out of the reactor chamber 2 through a port such as slit valve 8 or the like.

RF power at 13.56 MHz is predominantly used in plasma
30 reactors because this frequency is an ISM (Industry, Scientific, Medical) standard frequency for which government mandated radiation limits are less stringent than at non-ISM frequencies, particularly those within the communication bands. The substantially universal use of 13.56 Mhz is further
35 encouraged by the large amount of equipment available at that

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frequency because of this ISM standard. Other ISM standard frequencies are at 27.12 and 40.68 MHz, which are first and second order harmonics of the 13.56 MHz ISM standard frequency.

This energy is applied to a gas in the reactor chamber to
5 form a plasma. The plasma consists of two qualitatively different regions: a quasineutral, equipotential conductive plasma body 9 and a boundary layer 10 called the plasma sheath. The plasma body consists of substantially equal densities of negative and positive charged particles as well as radicals and
10 stable neutral particles. RF power coupled into the reactor chamber couples energy into the free electrons, imparting sufficient energy to many of these electrons that ions can be produced through collisions of these electrons with gas molecules. In addition to this ionization, dissociation and
15 excitation of molecules occur within the plasma body. In dissociation, a molecule, such as O_2 , breaks down into smaller fragments, such as atomic oxygen. In excitation, the molecule holds together, but absorbs energy and enters an excited electronic state. Control of the relative amounts of
20 ionization, dissociation, and excitation depends upon a variety of factors, including the pressure and level of energy applied to the plasma.

A plasma comprises a plurality of mobile charge carriers and thus is a conductive medium. Therefore, the interior of
25 the plasma is at a roughly uniform electric potential. However, the plasma cannot exist for long in contact with a material object, and is separated from objects by a so called sheath. The plasma sheath is an electron deficient, poorly conductive region in which the electric field strength is
30 large. The plasma sheath has a perpendicular electric field between the plasma body and any interface with a material object such as the reactor walls and the electrodes. The electric field at the interface with the wafers causes ions to accelerate perpendicularly into the surface of the wafers.
35 This perpendicular bombardment makes anisotropic etching

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possible. Typically, a wafer 7 is positioned on or slightly above the powered electrode 6 where there is a strong "self" bias which enhances ion impact energy and thus the etching process so commercially viable etch rates may be obtained.

5 However, many modern IC structures are sensitive to ion bombardment by high energy (> 100 eV) ions such as in the conventional plasma etch apparatus of Figure 1. Other etching processes require even lower ion energies to maintain selectivity. Because wafer damage decreases with decreasing
10 ion energy and associated sheath voltage, it would be advantageous to operate at smaller discharge power levels and voltages. Unfortunately, for capacitively coupled power at 13.56 MHz, this reduction of voltage results in a proportionately lower rate of creation of reactive species and
15 ions and therefore results in a slower etch rate for many processes, which significantly degrades process throughput.

Etch rates depend upon the ion current density at the wafer surface and the sheath voltage at the powered electrode (which determines the energy of the ions bombarding the surface
20 for etching). With decreased sheath voltage the ion current density at the wafer must be increased to maintain a substantially constant etch rate. This requires that the plasma ion density near the wafer be increased. Unfortunately, in a conventional plasma etcher, both the sheath voltage of the
25 powered electrode and the ion density near that electrode increase or decrease together and are monotonically increasing functions of the amplitude of the RF voltage applied to the powered electrode. Thus, if the sheath voltage is decreased by decreasing the voltage of the RF signal, the ion current
30 density at the wafer also decreases thereby producing an even greater decrease in the etch rate than would be the case from a decrease in the sheath voltage alone. It is desirable, therefore, to independently control the sheath voltage and ion density at the wafer so that a soft etch process (i.e., an etch

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process with reduced sheath voltage at the wafer) can be implemented that has a commercially adequate etch rate.

Induction coils surrounding a reactor chamber may be used to inductively couple RF energy into a plasma to control the
5 composition and density of the plasma for semiconductor processing. A separate powered electrode adjacent to a wafer is used to accelerate ions toward the wafer surface for etching. These two mechanisms may be used to control the ion density and energies in the plasma, thereby allowing high etch
10 rates with low ion energies.

However, problems are encountered with known methods of inductively coupling energy into a plasma reactor. The source of the inductive energy, such as induction coils, may also act to capacitively couple current into the reactor which modulates
15 the potential of the plasma sheath at the frequency of the RF source. Thus, the source of inductive energy -- used to control ion density -- may also affect the ions energies such that ion density and energy are not in fact independent. This may in turn degrade the symmetry of the etch and may damage
20 substrates sensitive to voltage fluctuations. It will also increase the time-averaged plasma potential and thereby increase the energies of ions bombarding the wafer and may adversely affect the selectivity of the etch.

Thus, what is needed for etching is a method for
25 independently controlling ion density and ion energy at a wafer surface. What is also needed is a method of inductively coupling energy into a plasma to produce ions without substantially modulating the sheath potential.

Plasma reactors are also used in processes that are even
30 more sensitive to ion energies and modulation of the sheath potential, such as resist removal. After resist is used in the etching process, it must be removed. The resist is typically an organic film such as a hydrocarbon polymer. Typical resist removal involves the production of large amounts of atomic
35 oxygen through dissociation. When exposed to the atomic

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oxygen, the hydrocarbon resist film will burn away. This particular process of resist removal is therefore often referred to as ashing. Additives or other gases may be used to remove other films in a similar manner.

5 Whereas etching depends upon bombardment by ions with relatively high energies, resist removal and similar processes depend upon the chemical reaction of an activated species (such as dissociated oxygen atoms) with a film on the semiconductor wafer. Typically, RF or microwave power is coupled into a gas
10 to form a plasma. The plasma acts as a source of dissociated atoms for reaction with the film on the wafer.

In contrast to etch processes, however, it is not desirable to expose the semiconductor wafers directly to the plasma. Direct contact with the plasma will cause a high flux
15 of charged particles to the wafer and a plasma sheath to form adjacent to the wafer surface, which may cause sputtering by ions from the plasma. The semiconductor wafers may thus be damaged either due to electrostatic charging, ion bombardment, or exposure to ultraviolet (UV) light, thus lowering the yield
20 of the semiconductor wafers.

One approach to decreasing destructive sputtering is to generate the plasma remotely and provide the dissociated atoms to the wafers through a path containing sharp turns. Ions in the plasma collide with the electrodes used to generate the
25 plasma and the chamber wall, but are substantially prevented from reaching the semiconductor wafers by the sharp turns.

A similar approach is described in U.S. Patent 5,217,560 (Kurano). A plasma reactor as described in Kurano is shown in Figure 2. The reactor comprises an outer reactor tube 11 and
30 an inner tube 12. Gases are supplied through gas pipe 14. Electrodes 15 and 16 create electric fields, E , which cause a plasma to be formed between the outer and inner tubes 11 and 12. Holes 13 allow reactive gases to reach the wafers 17, while ions tend to collide with the inner tube 12.

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However, a variety of disadvantages are associated with conventional plasma reactors used for resist removal and the like. First, complex structures may be necessary to isolate the plasma from the wafers. Second, providing atomic radicals to a wafer through a complex path may lead to the non-uniform flux of the radicals to the wafer. This decreases the uniformity of resist removal. Third, capacitive coupling of energy into the plasma drives currents and causes ions to collide with greater energy with the chamber wall and any electrodes in the plasma chamber. This may reduce the lifetime of the reactor (particularly if certain gas additives are used which may be needed for removal of residual chemicals from the "ash" of the resist). This problem is present even when the coupled energy is primarily inductive, since conventional induction coils also capacitively couple energy into the plasma and modulate the plasma potential thereby driving electric currents through the plasma to the nearest metal surfaces. This may cause discharges to barriers used to separate the plasma and the wafer and may limit the effectiveness of any such barriers. If small holes are used in the barrier so few ions can pass through, modulation of the plasma potential may induce hollow cathode discharge in the holes or between the grids. This may actually increase the charged particle current passing through the barrier. Finally, dissociated gases may recombine before reaching the remote wafers, reducing the efficiency of the plasma as a source of atoms.

Thus, what is needed for resist removal and similar processes is a method for producing abundant dissociated atoms without producing excessive ions and without unnecessarily driving RF currents or causing charge buildup at chamber walls, electrodes, or semiconductor wafers. What is also needed is a plasma reactor that is capable of generating a plasma in a chamber and providing neutral activated species to a semiconductor wafer without bombarding the wafer with ions and without necessitating complex structures that degrade wafer

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processing. Preferably, a plasma with a low or highly localized density of charged particles may be produced, and the majority of those charged particles may be filtered from the neutral activated species before reaching the wafer. What is
5 also needed is a method of inductively coupling energy into a plasma without substantially modulating the potential of the plasma.

SUMMARY OF THE INVENTION

10 One aspect of the present invention provides a method for inductively coupling energy into a plasma without substantially capacitively coupling energy into the plasma. Another aspect of the present invention provides a split Faraday shield interposed between a plasma and induction coil surrounding the
15 plasma.

It is an advantage of these and other aspects of the present invention that energy may be provided to a plasma without substantially modulating the sheath potential of the plasma and without substantially driving ion currents into a
20 chamber wall or semiconductor wafer. The split Faraday shield allows inductive energy to be coupled into the plasma while substantially preventing the capacitive coupling of energy into the plasma from the induction coil, thereby avoiding modulating the plasma potential.

25 Yet another aspect of the present invention provides a plasma reactor with a split Faraday shield interposed between a plasma in a reactor and induction coil surrounding the reactor, and also provides a powered electrode in the reactor adjacent to which a wafer or the like may be placed for processing.

30 It is an advantage of this and other aspects of the present invention that the induction coil may be used to control ion density in the plasma while the powered electrode may be used to substantially independently control ion energies for etching or the like. It is another advantage of this
35 aspect of the present invention that a soft etch may be

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implemented while commercially viable etch rates are maintained.

Yet another aspect of the present invention provides a method of dissociating molecules in a plasma without
5 substantially capacitively modulating the plasma potential. This aspect of the present invention may be implemented by interposing a split Faraday shield between induction coils and a reactor chamber.

It is an advantage of this and other aspects of the
10 present invention that abundant dissociated atoms may be produced without requiring excessive electron density and without unnecessarily driving currents of charged particles or causing charge buildup at chamber walls, electrodes or semiconductor wafers. It is yet another advantage of this
15 aspect of the present invention that a simple chamber structure may be used for resist removal or the like without substantially sputtering or otherwise damaging semiconductor wafers.

Another aspect of the present invention provides a
20 substrate that is electrically isolated from a ground potential for holding a wafer or the like for processing. It is an advantage of this aspect of the present invention that the amount of RF current drawn to the substrate and wafer is reduced.

25 Yet another aspect of the present invention provides an electric field for filtering charged particles from an activated species before introducing the activated species to a wafer or the like. Another aspect of the present invention provides one or more grids for filtering charged particles from
30 an activated species. Another aspect provides for inducing an electric field between grids to enhance filtering. Yet another aspect of the present invention provides for blocking UV radiation produced in a plasma from reaching a wafer or the like. This may be implemented by aligning grids so there is no
35 line of sight from the plasma to the wafer.

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It is an advantage of these and other aspects of the present invention that wafers or the like may be processed with activated species with reduced exposure to charged particles and UV radiation.

5

BRIEF DESCRIPTION OF THE DRAWINGS

These and other features and advantages of the present invention will become more apparent to those skilled in the art from the following detailed description in conjunction with the
10 appended drawings in which:

Figure 1 illustrates the structure of a prior art plasma etch reactor.

Figure 2 is a cross-sectional view of a prior art plasma reactor used primarily for ashing.

15 Figure 3 is an equivalent circuit for a prior art plasma etch reactor in which power is capacitively coupled into the reactor chamber.

Figure 4 illustrates the relationship between the RF signal applied to the powered electrode, the voltage V_p of the
20 plasma, and the sheath voltage V_{dc} .

Figure 5 is a side view of an inductively coupled reactor according to a first embodiment of the present invention.

Figure 6 is a cross-sectional side view of an inductively coupled reactor according to the first embodiment of the
25 present invention.

Figure 7 is a top view of the reactor according to the first embodiment of the present invention.

Figure 8 illustrates the relationship of plasma voltage V_p and the RF voltage applied to the cathode for the case when C_{s1}
30 is much larger than C_{s2} in a reactor according to the first embodiment.

Figure 9 is a graph illustrating the electron energy distribution for various values of E/N_g .

Figure 10 is a side cross-sectional view of a reactor
35 according to a second embodiment of the present invention.

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Figure 11A is a circuit used to provide an impedance Z_b in the reactor of the second embodiment.

Figure 11B is a circuit used to provide an impedance Z_c in the reactor of the second embodiment.

5 Figure 12 is a side cross-sectional view of a dual plasma reactor system according to a third embodiment of the present invention.

Figure 13 is a simplified top plan view of a dual plasma reactor system according to the third embodiment.

10 Figure 14 is a simplified side view of a plasma reactor system illustrating the structure of a split Faraday shield according to the third embodiment.

Figure 15A is a simplified side cross-sectional view of a charged particle filter according to the third embodiment.

15 Figure 15B is a top plan view of a grid used in a charged particle filter according to the third embodiment.

Figure 15C is a simplified side cross-sectional view of a three grid charged particle filter according to the present invention.

20 Figure 16 is a simplified side cross-sectional view of an alternative embodiment of a charged particle filter according to the present invention.

RELATED PATENT APPLICATION

25 The subject matter described herein relating to the plasma reactor of the first embodiment designed primarily for anisotropic ion etch or the like, illustrated with reference to Figures 3-8, is described in copending patent application Serial No. 07/460,707, filed January 4, 1990, in the name of Stephen E. Savas
30 [and assigned of record to the assignee of record of this application]. This patent application and any patents that may issue thereon are expressly incorporated herein by this reference.

DESCRIPTION

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The present invention relates primarily to plasma reactors and methods for processing semiconductor wafers using plasma reactors. The following description is presented to enable any person skilled in the art to make and use the invention.

5 Descriptions of specific embodiments are provided only as examples. Various modifications to the exemplary embodiments will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other
10 and scope of the invention. Thus, the present invention is not intended to be limited to the embodiments shown, but is to be accorded the widest scope consistent with the principles and features disclosed herein.

15 Plasma Reactor Primarily for Anisotropic Ion Etch or the Like

A first embodiment, suited primarily for ion etch processes will now be described. In accordance with the first embodiment, a plasma reactor is presented in which low
20 frequency (0.1-6 MHz) RF power is inductively coupled into the plasma to provide energy for ionization of the gas near a wafer-holding, powered electrode and a high frequency voltage of lower power is applied to the powered electrode to control the ion bombardment energy of a wafer upon this electrode. A wafer is placed on or just above this powered electrode for
25 processing.

This plasma reactor has a nonconductive chamber wall that is encircled by an induction coil that is connected to a low frequency RF source. A split Faraday shield is positioned
30 between the induction coil and the sidewall of the reactor and encircles the reactor to substantially eliminate the conduction of displacement currents between the induction coil and the plasma reactor. This shield, in effect, greatly reduces the capacitive coupling of the low frequency RF electric field to the plasma (see, the article by J.L. Vossen, entitled *Glow*
35 *Discharge Phenomena in Plasma Etching and Plasma Deposition*, J.

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Electrochem. Soc. Solid-State Science and Technology, Vol. 126, No. 2, February 1979, p. 319) so that the energy of ion bombardment of the reactor wall and associated etching and sputtering of the reactor wall is substantially eliminated and
5 so that the modulation of the wafer sheath voltage at the low frequency is reduced.

This Faraday shield is movable toward or away from the vessel so that the capacitance between the plasma and the shield can be varied. During wafer processing the Faraday
10 shield is placed substantially in contact with the chamber outer wall, producing a high value of capacitance. This reduces the RF plasma potential, thereby reducing plasma etching of the walls of the reactor. An increased gap between the Faraday shield and the chamber wall yielding a reduced
15 value of capacitance can be used outside of periods of wafer etching to produce increased RF and time-averaged plasma potential levels, thereby causing higher energies of ion bombardment that enable cleaning of the reactor walls with a controlled level of etching. Preferably, the Faraday shield is
20 moved radially to alter capacitance, but the capacitance could also be varied by vertical movement of the Faraday shield. In an embodiment in which the shield is movable vertically, the shield should not be allowed to move far enough vertically that it does not lie between the reactor chamber and every coil of
25 the induction coil. A conductive sheet can also be included above the top of the chamber to increase the capacitance of the plasma to the effective RF ground electrode provided by the reactor wall. This plate can also be movable to vary the capacitance between the plasma body and this portion of the
30 Faraday shield.

A DC magnetic field can be included to enhance ion generation at low pressures by confining electrons away from the walls of the chamber. At low pressure, electrons have an increased mean free path that would increase their rate of loss
35 from the chamber by collisions with the chamber wall. This

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magnetic field forces the electrons into helical paths that have an increased chance of making ionizing collisions within the chamber before colliding with a wall.

A diverging magnetic field which is stronger near the top
5 of the reactor chamber can be included to reflect electrons back into the plasma, thereby preventing their loss at the top wall of the chamber. This latter magnetic field (which might be as small as several ten thousandths of a tesla near the top of the chamber) can be produced either by an array of permanent
10 magnets positioned on top of the chamber and having alternating directions of their magnetic fields, by a solenoidal coil carrying DC current, or by a ferromagnetic disk.

The inductively coupled RF power is supplied at a frequency in the range 0.1-6 MHz and at a power level up to 10
15 kW, depending on the size of the reactor chamber. The voltage applied to the powered electrode is at a frequency much higher than the inverse of the average time for an ion to cross the sheath at the powered electrode. Preferred choices for the frequency f_n of this voltage signal are any of the ISM standard
20 frequencies 13.56, 27.12 and 40.68 MHz. The higher frequencies will be needed for higher densities of plasma in order to produce ion bombardment energies which are not too broadly distributed.

The sheath at the powered electrode contains a strong
25 electric field that is substantially perpendicular to the plane of the wafer, thereby producing substantially vertical ion impact and associated substantially vertical or controlled taper etching of the wafer. The amount of capacitively coupled power provided to the powered electrode is substantially less
30 than the power provided inductively to the plasma. Therefore, the average ion current at the wafer is primarily determined by the inductively coupled power and, due to the Faraday shield, the average ion energy at the wafer is substantially a function only of the amplitude of the RF signal to the powered
35 electrode. In contrast to this, in the typical plasma reactor

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illustrated in Figure 1, both the average ion density (which is typically somewhat lower) and energy are controlled by the amplitude of the RF signal to the powered electrode. The inductively coupled reactor therefore enables the sheath voltage to be reduced and the ion density to be increased. Also, the sheath voltage and ion density can be separately varied. Consequently, a soft etch at a commercially acceptable etch rate can be achieved that does not damage integrated circuits that are sensitive to ions having high impact energies on the order of or greater than 100 electron volts.

The electromagnetic fields in the inductively coupled plasma reactor can produce a plasma ion density distribution that is very uniform (depending on the shape of the chamber) over the wafer and therefore produces very uniform wafer processing. The inductively generated electric field is substantially circumferential and therefore accelerates electrons substantially parallel to the side wall of the reactor. Because of the conductivity of the plasma, the strength of this electric field decreases rapidly away from the side wall so that electron acceleration occurs primarily in a region near the side wall. As an electron gains speed, its inertia produces a trajectory that may include a succession of elastic collisions with molecules and/or glancing collisions with the sheath at the side wall. Such collisions can kick the electron into the plasma body. This results in significant electron acceleration only near the wall, but also results in ion generation throughout the chamber. The diffusion of these electrons and ions and the radial $\vec{E} \times \vec{B}$ drift of electrons produces near the wafer a radially symmetric ion density that has a very uniform density. A low pressure (typically, on the order of 0.13-3.9 Pascal) is maintained in the reactor chamber to facilitate diffusion of the electrons away from the region where they gain energy near the side wall.

This design is also extremely efficient in coupling power into the production of ions and therefore provides an important

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advantage over other reactors for wafer processing that is to be performed by the ions in the plasma (see, for example, the article by J. Freisinger et al. entitled *RF-ion source RIM 10 for material processing with reactive gases*, IX International Conference on Gas Discharges and their Applications, 19-23 September 1988). The importance of this can be seen from the following. RF power to a plasma produces neutral radicals, ions, free electrons and excitation of molecules and atoms by the free electrons. The vertical etching that is produced by reactive ions is favored in a chamber that channels a high fraction of the inductive RF power into ion production. For excessively high radical concentrations the reaction on the wafer surface of the radicals can be detrimental to the desired fabrication process so that it is also advantageous in many applications to reduce relative to ionization the production of free radicals by the plasma. Therefore, the plasma reactor according to the first embodiment is particularly suitable for reactive ion etch processes and other processes that are either favored by high ion concentration or degraded by significant free radical concentration. This reactor also requires much less capacitively coupled power than conventional plasma reactors. This system utilizes on the order of several hundred watts of RF power to the powered electrode compared to 500-1000 watts for a conventional plasma reactor in which all power is coupled capacitively. This system also provides the ability to control independently the ion current and the ion impact energy.

In the conventional plasma reactor of Figure 1, the amplitude of the RF signal applied to powered electrode 6 controls not only the ion density in the plasma, but also the sheath voltage of the powered electrode. To achieve a soft etch (i.e., energy of ion bombardment of the wafer on the order of 100 volts or less) the capacitively applied RF power should be reduced below that conventionally used in such a reactor. Unfortunately, this reduction of capacitively applied power

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would not only reduce the voltage drop across this sheath, it would also reduce the ion density at this sheath. Even at high RF voltage to the powered electrode, such capacitively coupled power produces only a relatively low density of ions. Because
5 the wafer etch rate is proportional to the product of the ion density at this sheath times the voltage drop across this sheath, the wafer etch rate decreases faster than either of these two parameters. Thus, a soft etch produces a reduction in throughput that is incompatible with a commercial integrated
10 circuit fabrication process.

That the sheath voltage in this system is tied to the amplitude of the RF signal applied to the powered electrode can be seen by reference to Figures 3 and 4. A capacitor 21 between RF source 5 and powered electrode 6 enables this sheath
15 voltage to have a DC component. This DC component is produced by the combined effects of the unequal areas of the electrodes and the unequal mobilities of the electrons and ions. Each plasma sheath is electronically equivalent to the parallel combination of a resistance, a capacitance and a diode. The
20 electric field across the sheath repels most of the electrons out of the sheath region producing a large sheath resistance on the order of 10^4 ohms. The capacitive component of the sheath impedance is negligible at very low frequency but with increasing frequency becomes small enough to be significant at
25 about 500 kHz. At frequencies above a few MHz, the sheath resistance is large enough that it can be ignored. This is the case for the high frequency sheath voltage component at the frequency of the capacitively coupled power in the first embodiment.

30 In the equivalent circuit in Figure 3, the effects of the much higher mobility of the electrons than the ions in the plasma and sheath is modeled by diodes 24 and 28. Thus, if the plasma were to become negative with respect to any electrode adjacent to the plasma, the electrons in the plasma would see
35 an effective short to that electrode. The sheath impedances

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are therefore modeled by elements 22-24 and 26-28. The plasma body can be modeled as a low impedance resistance 25 that can be ignored at the high frequency f_h (preferably one of the ISM frequencies 13.56 MHz, 27.12 MHz or 40.68 MHz) utilized for the RF voltage applied to the powered electrode.

Figure 4 illustrates the relationship between a 220 volt peak-to-peak RF signal 31 of frequency f_h applied to the powered electrode, the resulting voltage 32 of the plasma and the sheath voltage 36 of the powered electrode. The sheath capacitances C_{s1} and C_{s2} dominate at the frequency f_h of the capacitively coupled power so that resistances R_{s1} and R_{s2} can be ignored and diodes 24 and 28 can be ignored except for a short interval during each period of signal 31. Therefore, under most operating conditions, the plasma equivalent circuit reduces to a capacitive divider so that the plasma potential V_p and the high frequency components of the voltages across capacitances C_{s1} and C_{s2} are substantially in phase and related in magnitude by $V_p = V_{RF} \cdot C_{s2} / (C_{s1} + C_{s2})$.

For a typical reactor with a wall area several times that of the powered electrode, the sheath capacitance C_{s2} at the wall is on the order of 10 times the sheath capacitance C_{s1} at the powered electrode. Therefore, for a 220 volt peak-to-peak RF signal 31, the plasma potential V_p is on the order of 20 volts peak-to-peak. Because signals 31 and 32 are in phase, a peak 33 of signal 32 is aligned with peak 34 of signal 31. Because of diode 24, the minimum voltage difference between signals 31 and 32 (occurring at each peak 34) is on the order of kT_e/e . Similarly, V_p must also be at least kT_e/e more positive than ground 35 to avoid shorting the plasma to the walls of the reactor. These various requirements produce an average sheath voltage 36 of the powered electrode (i.e., the DC component of RF signal 31) of approximately -90 volts. The DC component of the sheath voltage is substantially equal to $-V_{RF}C_{s1}/(C_{s1} + C_{s2})/2$ where V_{RF} is the peak-to-peak magnitude of the RF voltage. The sheath voltage varies directly with the magnitude of the RF

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signal because the electric field component of the RF signal is substantially perpendicular to the powered electrode. This means that the DC component 36 of voltage 31 is directly tied to the peak-to-peak amplitude of the RF voltage applied to the
5 powered electrode.

The ionic current density at the powered electrode of a conventional plasma reactor is proportional to the ion density in the plasma which in turn decreases with decreasing power so that if the RF voltage amplitude is decreased to decrease the
10 sheath voltage, then the current density also decreases. Therefore, the plasma reactor of Figure 1 does not enable the current density at the wafer to be increased to maintain etch power when the voltage is decreased to produce a softer etch.

The voltage drop across the sheath at the powered
15 electrode is equal to the difference between the applied RF signal 31 and the voltage 32 of the plasma. This voltage drop varies from about 0 volts to about -220 volts. If an ion were to cross this sheath in a time interval that is short compared to the period $1/f_h$ of the RF signal, then its bombardment
20 energy could be almost zero if it crossed the sheath near a peak 34 of RF signal 31. Such low energy bombardment ions would not necessarily have trajectories substantially perpendicular to the plane of the wafer and therefore could degrade the desired vertical etch of the wafer. It is
25 therefore important for the period $1/f_h$ to be no more than half as large as the average time for an ion to cross this sheath. Since this transit time is on the order of or less than a half microsecond, f_h should be at least 4 MHz. For higher ion densities and low sheath voltage, the period $1/f_h$ can be as
30 small as 0.1 μ s or less. Because of the less stringent limitations on ISM frequencies, it is preferred that f_h equal one of the ISM frequencies 13.56 MHz, 27.12 MHz or 40.68 MHz.

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In Figure 5 is a plasma reactor according to a first embodiment of the present invention that allows independent, controllable variation of the sheath voltage and the ion current density at the wafer. This reactor also produces a very uniform distribution of ion current density and voltage at the wafer and can provide an increased ratio between ion and free radical production rates in the plasma relative to purely capacitive or higher frequency inductive discharges. This system is therefore particularly useful for applications, such as ion etch processes, in which an increased ratio of ions to free radicals is advantageous.

On a base 40 is a cylindrical chamber wall 41 that encloses a plasma reactor chamber 50 (shown in Figure 6). Chamber wall 41 is on the order of 7-30 centimeters high and has a lateral diameter that is dependent on the diameter of the wafers that are to be processed. For a 15 cm diameter wafer processing system, this chamber wall has a lateral diameter on the order of 25-30 centimeters and for a 20 centimeter diameter wafer processing system, this chamber has a lateral diameter on the order of 30-38 centimeters. Chamber wall 41 is made of a nonconductive material such as quartz or alumina.

Encircling wall 41 is an induction coil 42 that is connected to a first RF source 43 through a conventional impedance match network or transformer 44. This coil has a number of turns (on the order of 1-30 turns) to produce an inductance that can be conveniently matched to RF source 43 by a conventional match network 44 utilizing commercially convenient capacitor values or by a transformer to match the inductive impedance (typically, equal to or less than 10 ohms) to the impedance (typically, 50 ohms) of source 43. The match network is designed to substantially eliminate the reflection of power back to source 43. This induction coil produces within chamber 50 an axially symmetric RF magnetic field whose axis is substantially vertical and an electric field that is substantially circumferential. Both of these fields exhibit a

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rotational symmetry about a central axis A. This rotational symmetry contributes to the uniformity of wafer processing.

Because of the high conductivity of the plasma, the inductively coupled fields are substantially limited to a region adjacent to the side wall of skin depth δ (on the order of a centimeter) proportional to the square root of the electron density in the plasma divided by the frequency f_i of the inductively coupled RF field. For larger systems, f_i can be reduced to increase the thickness of this region within which electrons are accelerated.

Within this region, the circumferential electric field accelerates the electrons circumferentially. However, because of the inertia of these accelerated electrons, they may experience glancing collisions with the electric field of the sheath at the sidewall. Such collisions will reflect most of these electrons away from the wall. Some of these electrons will strike the side wall and may produce secondary electrons. Elastic collisions with gas molecules cause the electrons to diffuse throughout the volume of the chamber. Because the inductively generated electric field is limited to the distance δ (the resistive skin depth) from the side wall, electron heating is limited to this region. In order to enhance the uniformity of ion density across the wafer, the pressure is kept low (typically 0.13-3.9 pascal) so that the electrons heated near the wall can rapidly diffuse away from the wall to produce a very uniform ionization and resulting ion density across the wafer surface.

The reactor radius R , the frequency f_i and the power of the inductively coupled power are selected to produce a circumferential electric field having a peak-to-peak amplitude on the order of 1-10 volts/cm. This results in an oscillatory electron path of amplitude on the order of or greater than 3 cm so that the mean free path of these electrons is on the order of or less than the amplitude of their oscillatory motion.

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Source 43 provides power at a frequency in the range 0.1-6 MHz and at a power of up to 10 kW.

A second RF source 51 (shown in Figure 6) provides RF power to a powered electrode 52, preferably at one of the ISM
5 (Industry, Scientific, Medical) standard frequencies (i.e., 13.56 MHz, 27.12 MHz or 40.68 MHz). As in the embodiment of Figure 1, this RF power produces a DC sheath voltage next to the powered electrode 52. The power level is in the range from less than 100 watts to a few (up to 5) hundred watts so that
10 the effect of this capacitively coupled RF signal on the ion density is much less than the effect of the power inductively coupled from source 43. This power level is somewhat less than that typically provided to the powered electrode in a plasma reactor. This power level can be kept low to produce a soft
15 (i.e., kinetic energy less than 100 eV) bombardment of the wafer by ions. This low power level to the powered electrode also means that the ion density is determined primarily by RF source 43. This is advantageous in decoupling control of the ion density and the sheath voltage.

20 The circumferential direction of the inductively generated electric field makes this field parallel to the powered electrode so that a path integral from the plasma body to the powered electrode along a normal to the powered electrode is zero. As a result of this, unlike in the plasma reactor of
25 Figure 1, there is no RF component across the sheath producing an RF time varying potential difference between the plasma body and the powered electrode. This substantially eliminates coupling of the low frequency induced RF field to the potential of the powered electrode. Therefore, the sheath voltage of
30 powered electrode 52 is determined only by RF source 51.

Enclosing the side wall of the reactor is a grounded Faraday shield 45 consisting in this embodiment of a dozen conductive plates 46 that conform to the side wall. Each Faraday shield conductive plate 46 is spaced from its
35 neighboring plates by gaps 48. These gaps are needed to enable

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the induction RF magnetic field to penetrate within reactor chamber 50. At least one gap is needed to prevent the formation of a circumferential current in the Faraday shield. By Lenz's law, such a circumferential current would strongly
5 oppose variation of the magnetic field within reactor chamber 50, thereby substantially countering the desired action on reactor chamber 50 of the current in coils 42.

This Faraday shield also provides the same functions as the grounded conductive walls of the reactor of Figure 1 --
10 namely, it confines the capacitively coupled RF fields to the reactor chamber 50 so that they do not stray outside of the chamber and interfere with other equipment or violate federal radiation standards. This shield also provides a return path for the high frequency current from the electrode produced by
15 capacitively coupled power supply 51.

Faraday shield 45 can, when positioned next to the reactor wall, significantly reduce the amount of time variation of the plasma potential V_p at the RF frequency f_1 of source 43. This is important in decoupling the effects of the first RF source
20 43 and the second RF source 51 on the ion density and the average sheath voltage V_{dc} . At the power levels applied to coils 42, the large inductance (on the order of 1-100 microhenrys) of these coils produces a large voltage at one or both ends of these coils. If the Faraday shield were absent,
25 then the high voltage end 47 of coil 42 would couple capacitively to the plasma body and impress an RF variation of V_p at the frequency f_1 of source 43 (see, for example, J.L Vossen, *Glow Discharge Phenomena in Plasma Etching and Plasma Deposition*, J. Electrochem. So.: Solid-State Science and
30 Technology, Vol. 126, No. 2, p. 319).

The widths of gaps 48 are less than the minimum spacing between these plates 46 and coils 42 so that coils 42 do not significantly couple capacitively through these gaps to the plasma body (see the Vossen reference). If such capacitive
35 coupling to the plasma body were not blocked, this RF variation

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of V_p would show up as a variation of the sheath voltages (and hence, ion energy) at this same frequency. Furthermore, this electric field could degrade the symmetry of the etch if it were not substantially excluded by the Faraday shield.

5 Faraday shield 45 also significantly influences the value of the sheath capacitance C_{s2} for the plasma sheath adjacent to the wall 41 of the plasma reactor. If this Faraday shield were not present, then the effective ground for the capacitively coupled RF signal would be provided by the RF induction coil or
10 the environment surrounding the reactor chamber and therefore would be dependent on what other objects were near the reactor. Furthermore, these objects would generally be at a distance that is large enough that the effective ground could be treated as being at infinity. This makes C_{s2} for the side and top walls
15 more on the order of or less than one tenth of C_{s1} instead of ten times C_{s1} as was the case for Figure 4. The result is that the relationship between the plasma potential V_p and the RF signal is more like that shown in Figure 8 than like that shown in Figure 4.

20 In Figure 8, it is again assumed that the RF voltage (signal 71) has a peak-to-peak amplitude of 220 volts. For C_{s1} equal to ten times C_{s2} , the plasma voltage signal 72 has a peak-to-peak amplitude of 200 volts. The peaks 73 of the plasma voltage V_p again align with the peaks 74 of the RF voltage
25 signal 71 and the spacing between these peaks is again up to several times kT_e/e . Likewise, the spacing of the troughs of V_p from ground is on the order of kT_e/e (which is typically a few volts). The plasma voltage signal 72 therefore has a DC component 76 on the order of 100 volts. This contrasts with
30 Figure 3 where the plasma voltage signal 32 has a DC component on the order of 10 volts plus an offset of order kT_e/e .

This greatly increased DC component between the wall and the plasma body results in an unacceptable level of etching or sputtering of the wall by ions in the plasma. Such action not
35 only damages the chamber wall, but also uses up reactant gas

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and can inject into the plasma contaminants that can interfere with the wafer fabrication processes in the reactor chamber. However, with the Faraday shield 45 closely spaced from wall 41, the effective ground electrode capacitance is increased and C_{s2} is again several times larger than C_{s1} so that the relationship between the RF signal and the plasma voltage V_p is like Figure 4 instead of like Figure 8. Indeed, normally, the spacing between the "plates" (i.e., the plasma and the conductive wall) of the capacitance C_{s2} is on the order of 0.1 cm. For the reactor of Figure 5, when the Faraday shield is closely spaced from wall 41, the capacitance C_{s2} is increased by the thickness of wall 41 divided by the dielectric constant (which is > 4), which is equivalent to a vacuum gap of 0.075 cm. Therefore, the wall capacitance C_{s2} is a little more than half of what it would be for a reactor of the type shown in Figure 1 of comparable dimensions to that in Figure 5.

Plates 46 are movable radially on the order of or more than 1 cm so that capacitance C_{s2} can be substantially decreased by moving plates 46 away from the walls to vary the ratio C_{s1}/C_{s2} over a range on the order of from 0.1 to 10. These plates are moved close to wall 41 during wafer processing so that etching of the chamber wall and the associated production of contaminants are minimized. In periods other than wafer processing periods, the plates can be moved away from the wall by as much as a cm or more to provide a controlled period of etching of the wall to clean the wall. The remnants of this chamber cleaning step are then drawn out of the reactor before further wafer processing is implemented.

Figures 6 and 7 are respectively side cross-sectional and top views of the reactor according to the first embodiment illustrating further optional features of this reactor. Just outside of the top of reactor chamber 50 is a grounded conductive plate 53 that provides for the top of reactor chamber 50 substantially the same function as Faraday shield 45 provides for the sides of this chamber.

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On top of the reactor chamber are a set of magnets 54 that alternately orient their north poles downward. A ferromagnetic return plate 55 helps return the flux of the fields produced by the two outermost magnets. The magnets preferably are

5 permanent magnets since this type of magnet economically provides a sufficient magnetic field. This arrangement produces at the top of chamber 50 an array of alternating direction magnetic fields of about 0.01 tesla that act like magnetic mirrors to reflect electrons back toward the plasma

10 body. The fields from these magnets penetrate into the reactor chamber a distance on the order of twice the spacing (on the order of 2-3 cm) between these magnets. In other embodiments, this linear array of magnets could be replaced by a set of concentric ring magnets, again having north poles of successive

15 magnets oppositely oriented vertically. In still other embodiments, a flat disc of ferromagnetic material having its north pole oriented vertically or a single ring DC solenoid could be used to produce a single genetic mirror, which may have a magnetic field as low as several ten thousandths of a

20 tesla near the top of the chamber. The embodiment utilizing a magnetic disc is preferred because it is simple, inexpensive and preserves radial symmetry of the reactor. In contrast to this, the lack of radial symmetry of the magnetic fields from the magnets 54 in Figure 6 will slightly degrade the radial

25 symmetry of wafer etch.

Just outside of the base or top of reactor wall 41 is a conductive coil 56 connected to a DC current source 57 to produce an optional DC magnetic field to further contain electrons away from the side wall. The magnitude of the

30 magnetic field from this coil can be on the order of 0.0001-0.01 tesla.

The plasma reactor of Figures 5-7 provides significantly improved operation compared to many other existing reactors. Whereas a conventional plasma reactor using a microwave source

35 may produce only a few milliamps/cm² current density, the

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reactor according to the first embodiment may produce up to 50-100 milliamps/cm². Tests show that this high current results for a variety of reactant gases, such as SF₆, CF₂Cl₂, O₂ and argon. This indicates that relatively more of the power is
5 going into the production of ions instead of into the production of neutral fragments as in other plasma production methods at pressures above 1 millitorr. These neutral fragments would not contribute to this current. This is important for etching because only the ions are given the
10 perpendicular direction of impact on the wafer that results in the formation of substantially vertical walls. The ability to produce a very low sheath voltage at the wafer means that a 400 nm thick gate of polysilicon can be etched vertically without damage to or etching of an underlying 10 nm thick SiO₂
15 gate insulator by reducing the sheath voltage to less than 20-30 volts.

The reactor according to the first embodiment includes a gas source 49 and an exhaust port 58 that is part of an exhaust system 59 that includes a pump to exhaust plasma process
20 products and to keep the pressure to a selected level. Typically, the pressure is held at on the order of 1-30 millitorr pressure to enhance diffusion of electrons from the electron heating region near the side wall into the bulk volume. Even at this pressure, the inductively coupled power
25 is primarily coupled into production of ions. In contrast to this, other plasma systems such as the microwave plasma systems produce relatively more free radicals at pressures above approximately 1 millitorr. If a microwave plasma reactor is to primarily produce ions, then the pressure needs to be on the
30 order of or less than a few tenths of a millitorr. This requires that reactor pumps have speeds greater than a few torr-liters per second. This large rate of pumping requires either the use of cryogenic pumps closely coupled to the reactor chamber or very large turbo pumps with large ports to
35 the chamber. In contrast, the reactor disclosed herein can

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operate at higher pressures and requires a pump speed only on the order of a few hundred millitorr-liters per second. This is easily achieved with smaller pumps which need not clutter the space around the chamber and interfere with wafer handling or other essential chamber peripherals. Such pumps also would not require regeneration or pose a safety hazard as do cryogenic pumps.

Plasma Reactor Primarily for Resist Removal or the Like

Techniques similar to those described above may be used to produce abundant dissociated radicals for resist removal or the like. Whereas the above described reactor is configured to promote the production of ions for etching, a reactor for resist removal is preferably configured to promote dissociation and minimize ionization. Thus, according to a second embodiment of the present invention, a plasma reactor is provided for the efficient dissociation of molecules for use in resist removal or similar processes.

At a general level, the structure of a reactor for dissociation according to the second embodiment is similar to the reactor for ion etching according to the first embodiment as described above. Induction coils surround a reactor chamber and inductively couple energy into the chamber to produce a plasma. Electrons are accelerated circumferentially within the plasma by the induction electric field causing collisions with molecules. These collisions result in excited molecules, dissociated atoms, and ions. Higher energy collisions tend to produce ionization, while lower energy collisions result in excitation and dissociation. In particular, electron energies in the range of 11-12 eV are typical for the threshold for ionization of oxygen gas, while electron energies of 5-6 eV are typical for the threshold for dissociation.

The electron energies depend upon the strength of the electric field which accelerates the electrons and the density of the gas which determines the mean distance over which

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electrons are accelerated between collisions. For an ion etch reactor, a higher power is applied to the induction coil to increase the induction electric field, and a lower gas pressure is used which allows electrons to accelerate with fewer
5 collisions and attain the energies necessary for ionization. For a plasma reactor used for dissociation, a lower power and higher pressure and flow are used.

The effect of varying the electric field and density of gas on the distribution of electron energies in a plasma is
10 illustrated in Figure 9. The electron energy distribution, $f_e(E)$, is a function of the electric field, E , divided by the number density of the gas, N_g . In the graph in Figure 9, the distribution of electrons at various energies is shown for different values of E/N_g (in a 90% O_2 and 10% O gas). Electron
15 energy is plotted along the x axis and \log_{10} of the electron density is plotted along the y axis. Along the x axis, T_d indicates the threshold energy for dissociation ($\sim 5-6$ eV), while T_i indicates the threshold for ionization (~ 12 eV). Curve 92 shows the distribution for $E/N_g \sim 2.2 \times 10^{-16}$ Vcm². This
20 may represent an electric field of 14 V/cm and a density of 6.4×10^{16} /cm³. Curve 94 shows the distribution for $E/N_g \sim 4.4 \times 10^{-16}$ Vcm². Curve 96 shows the distribution for $E/N_g \sim 8.8 \times 10^{-16}$ Vcm². Curve 98 shows the distribution for $E/N_g \sim 10.9 \times 10^{-16}$ Vcm². Curves 92-98 show that the number of electrons having
25 energies greater than T_i decreases as E/N_g decreases. The electron energy distribution of curve 98 leads to a significantly higher ion density than the distribution curve 92.

Thus, it can be seen that the level of energy coupled into
30 the plasma and the pressure are two important variables in determining the composition of the plasma. In the first embodiment, a low pressure is used (1-30 millitorr) with a relatively high level of RF power applied to the induction coil (up to 10 kW). This provides a relatively high level of
35 ionization. For the second embodiment, a higher pressure

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(approximately 1-2 torr) and lower level of RF power (approximately 500-1500 watts) are used. This favors dissociation over ionization relative to the first embodiment. Preferably, in the second embodiment, only enough ionization
5 occurs to sustain the plasma and continue the dissociation of atoms. In the second embodiment, a typical value for the electric field, E , is approximately 40 V/cm at the highest point in the reactor chamber and a typical value for the density of the gas, N_g , is approximately $10^{16}/\text{cm}^3$ to $10^{17}/\text{cm}^3$. E/N_g
10 is preferably no greater than approximately 10^{-15} Vcm^2 and the peak ion density at the center of the plasma is preferably on the order of $10^9/\text{cm}^3$.

As in the first embodiment, a split Faraday shield is used to substantially prevent the capacitive coupling of energy into
15 the plasma and to greatly reduce modulation of the plasma potential thus permitting the plasma to be restricted to the volume within the induction coil. In the first embodiment, the split Faraday shield is desirable, because it allows a second mechanism (the powered electrode) to independently control the
20 energies of ions bombarding and etching the wafer. In resist removal and similar processes, however, charged particle flux is undesirable, and may damage or drive impurities into the surface of the wafer. Thus, in the second embodiment, ion density is reduced and there is no powered electrode
25 accelerating ions toward the wafer. The split Faraday shield is important for reducing electric current flux to the wafer and reactor walls due to capacitive coupling from the induction coil and to keep the plasma confined to the region inside the coil where it is produced.

30 As described above, the split Faraday shield only allows the induction coil to generate an induction electric field in the plasma. Any capacitive electric field between the plasma and the induction coil -- which would drive current toward the chamber wall and modulate the potential of the plasma -- is
35 substantially blocked by the split Faraday shield. The

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induction electric field accelerates electrons circumferentially. This provides the energy for electrons causing dissociation and ionization, but does not accelerate ions directly at the chamber wall. Further, the induction electric field does not cause charge build up in the plasma, which would modulate the plasma potential and drive currents into the wafer. This can be seen from Maxwell's equations on electromagnetism as set forth in Table 1.

10

15

TABLE 1	
(1)	$\nabla \times \vec{E}_1 = -\partial \vec{B} / \partial t$
(2)	$\nabla \cdot \vec{E} = \rho / \epsilon_0$

According to equation (1), the curl of the induction electric field, E_1 , is given by the time rate of change of the magnetic field. This cannot be zero, because the induction coil induces a time varying magnetic field. This in turn means that the induction electric field cannot be the gradient of any potential, because if the induction field was the gradient of some potential the curl would be zero in contradiction of equation (1). Equation (2) shows that for an electric field derived from a potential the dot product is equal to the charge concentration, ρ , over the permittivity constant, ϵ_0 . Since the induction electric field cannot be the gradient of any potential, it follows from equation (2) that the induction electric field cannot result in any charge buildup inside the plasma. Therefore, the induction electric field does not induce any temporal modulation in the plasma potential.

Several advantages are realized from using an inductive electric field for sustaining the discharge while blocking any capacitively induced electric field. First, since the induction electric field is circumferential and no bias on the chamber wall is induced, ion bombardment of the chamber wall is

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reduced and occurs at lower energy. This extends the life of the reactor, and is particularly important when certain additives are used to enhance resist or film removal. For instance, halogenated gases, such as SF_6 , Cl_2 , CHF_3 , CF_4 , C_2F_6 , 5 CFC's, or the like, may be added to oxygen in small concentrations to affect the chemistry of the reaction that strips the film off the wafer and removes residue from the wafer. If a capacitive electric field causes bombardment of the chamber wall, these additives may quickly erode the chamber 10 wall. Since a split Faraday shield is used to substantially block the capacitive electric field in the second embodiment, additives may be used with less substantial erosion of the reactor. Second, the plasma potential is minimally modulated and the electric field at the wafer surface can be kept small. 15 This reduces the charged particle current driven into the wafer surface which may damage the wafer. Third, since the plasma potential has reduced RF modulation, the plasma can be substantially confined to a portion of the chamber away from the wafer surface. The complex structures used in the prior 20 art for segregating the plasma and the wafer are not used in the reactor according to the second embodiment. This allows unimpeded diffusion of dissociated atoms across the wafer surface, which provides more uniform stripping of the resist or other film. However, as discussed below, a system of grids 25 between the plasma and the wafer may be used in an alternative embodiment to additionally filter charged particles and UV radiation.

The structure and operation of the plasma reactor according to the second embodiment will now be described. 30 Figure 10 shows a side cross-sectional view of a reactor according to the second embodiment. On a base 40 is a cylindrical chamber wall 41 that encloses a plasma reactor chamber 50. A cylindrical or hemispherical chamber is preferred when a circumferential induction field is used, since 35 it reduces the collisions between ions and the chamber wall.

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Chamber wall 41 is about 30 cm high and has a lateral diameter that is dependent on the diameter of the wafers that are to be processed. Chamber wall 41 is made of a nonconductive material such as quartz or alumina.

5 It should be noted that the structure of reactor chamber 50 allows dissociated gases to flow and diffuse directly from the plasma to a wafer or other substrate at the bottom of the chamber. This may enhance the uniformity of stripping. Prior art reactors which separate the plasma from the wafer with
10 walls or tubes typically exhibit a uniformity in the range of five to ten percent (meaning that the amount of stripping on one portion of the wafer is within five to ten percent of the stripping on other portions), while the second embodiment of the present invention may provide a uniformity of a few percent
15 when used in common stripping processes. However, as described below, in an alternative embodiment a grid system may be used to filter charged particles without an appreciable reduction of uniformity.

At the base of the reactor chamber is an aluminum block 60
20 or pedestal for supporting a wafer for processing. The block may be maintained at an elevated temperature, such as 250°C, to enhance ashing. A heating unit 61 may be used to heat the block in a conventional manner. It is also desirable to minimize the electric field between the plasma and the wafer
25 surface to reduce the charged particle current driven into the wafer. Therefore, the potential of the aluminum block 60 supporting the wafer is maintained at near the same potential as the volume of the chamber directly above the wafer. This is accomplished by placing an element, Z_b , between the aluminum
30 block and a ground potential producing a high impedance of the block to ground at the frequency of excitation. The aluminum block is substantially free to float at the chamber potential. The impedance, Z_b , is selected to be very high given the frequency of the RF power coupled into the plasma. As shown in
35 Figure 11A, the impedance Z_b is preferably provided by a

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parallel LC circuit inserted between the aluminum block and a ground potential. The capacitance C is chosen taking into account stray capacitance C_{stray} between the aluminum block and the other elements of the plasma reactor chamber. When RF
5 power is provided at a frequency of 13.56 MHz and at a power of between 500 and 1500 watts, the inductance L is preferably on the order of less than 1 microhenry and the capacitance C is preferably on the order of several hundred picofarads.

Encircling wall 41 is an induction coil 42 that is
10 connected to an RF source 43 through a conventional impedance match network 44, as described previously for the first embodiment. This coil preferably surrounds only the top half of the reactor, so that a plasma can be generated and confined to this region. The bottom portion of the reactor chamber acts
15 as a diffusion region where dissociated gas from the plasma may diffuse uniformly across the wafer surface.

This induction coil produces within the top portion of chamber 50 an axially symmetric RF magnetic field whose axis is substantially vertical and an induction electric field that is
20 substantially circumferential. Both of these fields exhibit a rotational symmetry about a central axis. In this configuration, the plasma 62 is concentrated in the shape of what approximates an annular toroid near the top of the reactor chamber. It will be understood, however, that the electric
25 fields are not completely symmetrical and in fact are stronger near the reactor chamber wall than in the center of the reactor chamber. The electric field and density of the plasma reach a peak in the center of the annular toroid. The rotational symmetry contributes to the uniformity of wafer processing.

30 In the second embodiment, RF energy is applied to the induction coil from source 43 at a frequency of 13.56 MHz and at a power of between 500 and 1500 watts. Of course, a wide variety of frequencies may be used. The number of turns of the induction coil and the current may be adjusted for a given
35 frequency so that the desired induction electric field is

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produced inside the reactor chamber. In the second embodiment, a peak induction electric field of approximately 40 volts/cm is preferred. For oxygen based stripping, frequencies from 1 to 27 MHz may be used. Even lower frequencies, possibly as low as 100 kHz, might be used if a very large number of turns, on the order of 20 or 30, were used in the induction coil with a large current, perhaps as high as 100 amperes. While the current requirements and number of turns in the coil make low frequency operation undesirable, the modulation of the plasma potential is reduced at these frequencies because the capacitive impedance is inversely proportional to the frequency. Thus, it may be possible to operate at low frequencies and high currents without a split Faraday shield, or alternatively, less effective shielding could be used.

As with the ion etcher of the first embodiment, a split Faraday shield is interposed between the induction coil and the plasma. While the split Faraday shield of the first embodiment may be used here as well, Figure 10 shows an alternative configuration. Referring to Figure 10, there is both an inner 63 and outer 64 split Faraday shield. When a split Faraday shield is used, there is some capacitive coupling between the induction coil and the split Faraday shield. Since there is some inductance along the current path from the induction coil to ground (through the split Faraday shield), some voltage may appear on the shield and modulate the plasma potential. In the first embodiment, for instance, 5 to 10 volts may appear on the split Faraday shield, leading to approximately a 1 volt modulation of the plasma potential. With the double split Faraday shield shown in Figure 10, this modulation can be even further reduced. Five to ten volts may appear on the outer split Faraday shield 64 due to capacitive coupling with the induction coil. There may also be some capacitive coupling between the inner 63 and outer 64 split Faraday shields, but since the inductance along this path is finite, only about 1

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volt appears on the inner split Faraday shield 63. This leads to only about .1 volt modulation of the plasma potential.

To further reduce any capacitively coupled voltage on the split Faraday shields, the outer shield 64 may be connected to
5 ground through an impedance Z , that is minimized for a given RF frequency. As shown in Figure 11B, this impedance may be provided by a series LC circuit. Preferably the capacitor is adjustable, so the circuit may be tuned for the frequency of RF energy used. For RF energy at a frequency of 13.56 MHz and a
10 power of between 500 and 1500 watts, an inductance L of $1/4$ microhenry, and a capacitance C of up to approximately 500 picofarads are preferred. As is known in the art, the capacitor may be adjusted so that the circuit has a resonance at the frequency of the RF power such that the impedance to
15 ground is minimized. Inserting an adjustable impedance between the split Faraday shield and ground also provides an advantage in that a high impedance for a given frequency may be used at startup. This allows a high voltage to appear on the shield, which can be used initially to ignite the plasma. Then the
20 impedance can be tuned to resonance to decrease the capacitive voltage for processing.

Gas is provided to the reactor from gas source 49. In the second embodiment, O_2 gas is provided at approximately 1-10 standard liters per minute, with 2 standard liters per minute
25 being typical. It is believed, however, that flow rates from 100 standard cubic centimeters per minute up to 100 standard liters per minute may be effectively used in the second embodiment. The gas source 49 and an exhaust system 59 cooperate to maintain a flow from plasma to wafer and a
30 pressure in the reactor chamber that promotes dissociation given the strength of the induction electric field. For oxygen gas based processes, pressures in the range of 1-5 torr are used, with 1.5 torr being preferred. However, pressures as low as .1 torr may be used even though ion density increases,
35 because the split Faraday shield reduces ion bombardment.

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Further, as described below, filtering techniques may be used to remove residual charged particles.

In addition, other gases may be used to enhance stripping. In particular, as is known in the art, gases may be added to
5 oxygen in small concentrations to attack specific chemical residues that may form on the resist. During etching, the resist may become very hard and dense. Ion implantation may occur and impurities such as boron or arsenic may become embedded in the resist. Etch processes may also produce a SiO_2
10 residue or implant aluminum contaminants in the resist. As is known in the art, these and other side effects from wafer processing can create a resist or film that is very resistant to reaction with oxygen atoms alone. Additives that make soluble or volatile compounds with a given chemical residue or
15 contaminant may be added to enhance the removal of the resist. Any one of a variety of additives may be selected depending upon the composition of the resist and the etch process used. Common additives include Ar, He, SF_6 , Cl_2 , CHF_3 , C_2F_6 , CFC's, N_2 , N_2O , NH_3 , H_2 , water vapor, or the like. For instance, after a
20 polysilicon etch process, using a normal resist, CF_4 is preferably added to the O_2 gas in concentrations of 0.2% to 10% in order to enhance resist removal. Importantly, these additives may be used without substantial etching and wear of the reactor chamber, because, as described above, the split
25 Faraday shield reduces the modulation of the plasma potential.

In the reactor according to the second embodiment, large volumes of gas are dissociated and uniformly diffused in the flowing stream across a wafer surface. The efficiency of conversion may be as high as 50% or more and a stripping rate
30 exceeding 10 microns per minute may be achieved. A uniformity of a few percent may be achieved and the damage to the wafer is low. Damage to the wafer is measured by looking at the flatband voltage shift in the CV curve of the wafer after processing. Mobile sodium or other contaminants may be
35 imbedded in the wafer and degrade the performance of insulating

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material like SiO_2 . For the second embodiment, the mobile ion density deduced from the CV shift of a typical wafer is on the order of $10^{10}/\text{cm}^2$. Conventional reactors that use physical barriers to separate the plasma from the wafer typically have
5 lower strip rates and worse uniformity, while reactors that do not separate the plasma may cause mobile ion concentrations as high as $10^{12}/\text{cm}^2$.

Figure 12 illustrates a third embodiment of the present invention which is presently preferred for removing residual
10 resist and contaminants from semiconductor wafers and the like. Figure 12 shows a side cross-sectional view of a dual reactor system according to the third embodiment. In the third embodiment, two cylindrical plasma generation chambers 202a and 202b are arranged side by side. Similar elements are used in
15 conjunction with the two plasma generation chambers 202a and 202b. These similar elements are labeled with the same reference numeral for each chamber in Figure 12, although suffixes "a" and "b" have been added to differentiate between elements for generation chamber 202a and 202b respectively.
20 These elements may be referred to generally by their reference numeral without any appended suffix. While the two generation chambers use substantially duplicate elements and operate substantially independently, they share a gas supply system 204, an exhaust system 206 and a wafer processing chamber 208.
25 The system of the third embodiment is configured for concurrent processing of two wafers which doubles throughput.

The reactor system of the third embodiment operates in a manner similar to that described for the second embodiment. However, a single split Faraday shield and a charged particle
30 filter are used. The split Faraday shield is grounded and has widened slots that allow slight capacitive coupling of energy into the plasma from the induction coils. This helps ignite and sustain the plasma reaction, although it does introduce a slight modulation of the plasma potential. While there is a
35 slight modulation of the plasma potential (on the order of 10

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volts), the modulation is still greatly reduced by the split Faraday shield. This combination of a single split Faraday shield and a charged particle filter allows a sustained plasma reaction to be easily maintained, while actually reducing the charged particle exposure of the wafer. In addition, the charged particle filter blocks UV radiation produced in the plasma from reaching the wafer. This UV radiation might otherwise cause damage to an SiO_2 layer or SiO_2/Si interface of a wafer. Finally, the design of the charged particle filter actually enhances the diffusion of neutral activated species and therefore does not degrade the uniformity of stripping as do some conventional barrier systems.

The structure of the reactor system according to the third embodiment will now be described in detail. In the third embodiment, two wafers may be placed in a processing chamber 208 for resist stripping, residue or contaminant removal, descum or the like. The processing chamber 208 is rectangular, and has a height, h_1 , of approximately 25 cm. Figure 13 shows a simplified top plan view of the reactor system according to the third embodiment. Referring to Figure 13, the width 302 of the processing chamber is approximately 60 cm. The depth 304 of the processing chamber measured from the outside of processing chamber wall 210 is approximately 35 cm. The plasma generation chambers 202a and 202b are situated above the processing chamber and have an outer diameter 306 of approximately 20 cm as shown in Figure 13. The plasma generation chambers are separated by a distance 308 of approximately 28 cm from center to center.

Referring to Figure 12, the processing chamber wall 210 is approximately 2.5 cm thick and is grounded. The processing chamber wall 210 provides a common ground for the system, and comprises a conductive material such as aluminum or the like. Within the processing chamber is an aluminum block 212 that acts as a conductive heater and as a support for wafers 7. From center to center, two 20 cm wafers 7 may be placed

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approximately 28 cm apart on the aluminum block 212 for processing. Of course, the present invention may be readily adapted for use with wafers of other sizes as well. The aluminum block 212 is maintained at an elevated temperature that is favorable to the desired reactions at the wafer surface. Preferably the aluminum block 212 is maintained at about 250°C for most resist stripping. Other temperatures may be desirable for certain processes. For instance, a temperature of between 150°C and 180°C may be used for implant resist removal, and a temperature of approximately 100°C may be used for descum. A heating unit, as described and shown for the second embodiment may be used to heat the aluminum block, although the heating unit has not been shown in Figure 12 for purposes of clarity. The aluminum block 212 is positioned on a cylindrical ceramic support 213 and is RF isolated. In particular, an element Z_0 may be placed between the aluminum block 212 and a ground potential as described for the second embodiment to produce a high impedance of the block to ground at the frequency of excitation. The ground potential may be provided by the processing chamber wall 210. This RF isolation allows the potential of the aluminum block 212 to be maintained at or near the same potential as the volume of the processing chamber directly above the wafers 7. This helps reduce the charged particle current driven to the wafers 7.

Below the ceramic support 213 is a gas exhaust system 206. The gas exhaust system 206 may be driven by a conventional fan, pump or similar device. The ceramic support 213 is slotted to allow gases to reach the exhaust system from the perimeter of the aluminum block 212. This promotes a gas flow radially from the center of wafers 7 to the perimeter of the aluminum block. The gases then flow under the aluminum block through the slots in the ceramic support 213 and to the exhaust system 206. The exhaust system 206 has a shut off valve 214 that allows the exhaust system to be shut off. In addition, a throttle valve

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215 is provided for regulating the flow of gases in the exhaust system 206.

The top of processing chamber 208 is approximately 5-6 cm above the surface of wafers 7. This provides a distance over which gases may diffuse and flow across the surface of the wafers. Above each wafer 7 is a plasma generation chamber 202a and 202b. The plasma generation chambers have a height, h_2 , of approximately 25 cm and as shown in Figure 13 have an outer diameter 306 of approximately 20 cm. Referring to Figure 12, the generation chamber walls 216 are made of a nonconductive material such as quartz or alumina and have a thickness of approximately 4 to 6 mm. The generation chamber walls 216 are fixed at their base to the processing chamber wall 210 at the top of the processing chamber 208. The top lid 217 of the generation chambers is aluminum or similar conductive material in the third embodiment. The two plasma generation chambers 202a and 202b are separated by a conductive partition 218 to prevent any electromagnetic interference between them. In the third embodiment, the partition 218 comprises a thin (approximately 1/16 inch thick) sheet of aluminum. The partition may be an integral part of a safety cage that encloses the generation chambers and the induction coils and prevents the radiation of electromagnetic interference into the surrounding environment. For purposes of clarity, the safety cage is not shown in Figure 12.

A gas supply system 204 is provided above the plasma reactor chambers 202. In the center is an input pipe 220 that can be used to provide gases (such as oxygen) to the plasma reactor chambers. As described above, additives may also be introduced through input pipe 220 for residue removal after basic ashing with oxygen gas. Typically, oxygen gas will be used to ash to endpoint (which is determined by the absence of CO emission). Then oxygen gas is used to over ash for a period approximately equal to 100% of the period required to ash to endpoint. Then an additive, such as CF_4 , is added to the

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oxygen in a concentration of about 0.2% to 10% and introduced to the generation chambers through input pipe 220 for about 15 seconds in order to remove residual contaminants. In the third embodiment, a shower head type nozzle 222 disperses the gas laterally into the generation chambers to enhance uniformity.

In the third embodiment, O₂ gas is provided at approximately between 1 and 20 standard liters per minute through input pipe 220, with 4 standard liters per minute being typical (2 standard liters per minute for each plasma generation chamber). It is believed, however, that flow rates from 200 standard cubic centimeters per minute up to 200 standard liters per minute may be effectively used in the third embodiment. The gas supply system 204 and the gas exhaust system 206 cooperate to maintain a flow from plasma to wafer and a pressure in the reactor chamber that promotes dissociation given the strength of the induction electric field. For oxygen gas based processes, pressures in the range of 1-5 torr are used, with 1.5 torr being preferred. However, pressures as low as .1 torr may be used even though ion density increases, because the split Faraday shield and a charged particle filter (described further below) reduce ion bombardment.

Encircling the generation chamber walls 216 are induction coils 224. The induction coils are connected to an RF source (not shown) through a conventional impedance match network (not shown), as described previously. In the third embodiment, the induction coils 224a and 224b each have three turns. The induction coils 224a and 224b preferably have a conductor diameter of approximately 1/4 inch, and are turn-to-turn separated by approximately 5/8 of an inch from center to center. The diameter from the center of the coil on one side of a plasma generation chamber to the center of the coil on the other side of the plasma generation chamber is approximately 9 inches. The center of the middle turn of the induction coil is almost half way down the plasma generation chamber,

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approximately 11 cm from the top of the plasma reactor chamber and approximately 14 cm from the top of the processing chamber 208. This allows a plasma to be generated and substantially confined to the middle region of the plasma processing reactor.

5 In the third embodiment, RF energy is preferably applied to the induction coil at a frequency of 13.56 MHz and at a power of between 500 and 1500 watts. The induction coil produces within the middle portion of the plasma generation chambers 202 an axially symmetric RF magnetic field whose axis
10 is substantially vertical and an induction electric field that is substantially circumferential. Both of these fields exhibit a rotational symmetry about a central axis. In this configuration, the plasma is concentrated in the shape of what approximates an annular toroid in the middle of the plasma
15 reactor chambers. It will be understood, however, that the electric fields are not completely symmetrical and in fact are stronger near the generation chamber wall 216 than in the center of the plasma generation chambers. The electric field and density of the plasma reach a peak in the center of the
20 annular toroid. In the third embodiment, a typical value for the peak electric field, E , is approximately 40 V/cm at the highest point in the reactor chamber and a typical value for the density of the gas, N_g , is approximately $10^{16}/\text{cm}^3$ to $10^{17}/\text{cm}^3$. E/N_g is preferably no greater than approximately 10^{-15} Vcm^2 and
25 the peak ion density at the center of the plasma is preferably less than about $10^9/\text{cm}^3$.

As with the first and second embodiments, a split Faraday shield is interposed between the induction coil and the plasma. In the third embodiment, a single split Faraday shield with
30 widened slots is used. Figure 14 illustrates the structure of a split Faraday shield 226 according to the third embodiment. The split Faraday shield has vertical slots that start approximately 3/4 inch from the top of the plasma generation chambers 202 and end approximately 1 1/4 inches above the top
35 of the processing chamber 208. This allows the split Faraday

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shield to be an integral part which simply fits over the plasma reactor chamber. The bottom of the split Faraday shield is connected to the top of the processing chamber which provides a common ground for all of the sections of the split Faraday
5 shield.

In the third embodiment, the split Faraday shield is designed to allow some modulation of the plasma potential. This makes it easier to ignite and maintain a plasma reaction in the plasma generation chambers. Nevertheless, the split
10 Faraday shield blocks substantial capacitive coupling and limits the modulation to a desired amount (order of magnitude 10 volts in the third embodiment). The number and width of slots in the split Faraday shield may be selected to control the level of capacitance coupling and modulation. The slots
15 228 in the split Faraday shield 226 of the third embodiment start out with a slot width of approximately $3/16$ inch in top portion 230 and widen to approximately $3/8$ inch in the middle portion 231. The induction coil 224 surrounds the split Faraday shield around this widened middle portion 231 of the
20 slots. The slots then narrow again to $3/16$ inch in a bottom portion 234. The top and bottom narrow portions 230 and 234 of the slots are approximately $2\frac{1}{2}$ inches long and the widened middle portion 234 is approximately 3 inches long. For the split Faraday shield of the third embodiment, there are 8 slots
25 with adjacent slots being separated by a distance of approximately 8.35 cm apart from center to center. This is in contrast to a split Faraday shield designed to maximally block capacitive coupling of the induction coil to the plasma. To more completely block capacitive coupling a large number of
30 narrow slits (on the order of 30 or more) might be used, and a double split Faraday shield as described for the second embodiment might be used. While the split Faraday shield of the third embodiment may allow more modulation and produce more charged particles, the plasma reaction is easier to ignite and

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sustain and the structure of the split Faraday shield is less complex.

Referring to Figure 12, the split Faraday shield 226 is interposed between the induction coils 224 and the generation
5 chamber walls 216. Preferably, the split Faraday shield is approximately 5/16 inch from the induction coil 224. The split Faraday shield 226 has a width of approximately 115 thousandth of an inch in the section containing the slots 228. The top and bottom portions of the split Faraday shield 226 that do not
10 contain the slots may be even thicker (approximately 1/2 inch thick) to provide a more rigid structure for the split Faraday shield.

To reduce the charged particle current reaching the wafers 7 and to block UV radiation that may be generated in the plasma
15 from reaching the wafers 7, a charged particle filter 240 is provided between the plasma reactor chambers 202 and the processing chamber 208. The charged particle filter 240 is particularly desirable in the third embodiment, since the split Faraday shield allows some modulation of the plasma potential.
20 However, the charged particle filter may also be advantageously used in the second embodiment as well as in other types of reactors and stripping systems. Such a charged particle filter may be applied in any number of situations where it is desirable to filter charged particles from a gas flow.

25 The charged particle filter 240 of the third embodiment is shown in additional detail in Figure 15A. The charged particle filter includes an upper grid 402 and a lower grid 404 made out of a conductive material such as aluminum. This is because the oxide that forms on their surface is both resistant to attack
30 by fluorine atoms and does not catalyze recombination of oxygen atoms into molecules as other metals such as copper would. The grids are preferably separated by approximately 40-50 thousandths of an inch, and are approximately .16 inches thick. The grids are held apart by a block of insulating material 406
35 such as quartz, alumina or mica. Each grid has an array of

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holes. The holes are approximately .156 inches in diameter and are separated by a distance of approximately .28 inches from center to center. The array of holes 410 in the lower grid 404 are offset from the array of holes 408 in the upper grid 402.

5 Figure 15B is a top plan view of upper grid 402 showing the arrangement of the array of holes 408. In addition, the arrangement of the array of holes 410 relative to the array of holes 408 is indicated with dashed lines in Figure 15B.

Preferably, there is no line of sight all the way through the
10 upper and lower grids 402 and 404. This prevents potentially damaging UV radiation in the plasma generation chambers from reaching the wafers 7. In addition, it forces charged particles and dissociated atoms to follow a non-linear path through the filter. This provides additional time for the
15 neutral activated species to diffuse uniformly, and provides time for charged particles to be filtered from the gas flow.

Charged particles are filtered from the gas flow through collisions with the grids 402 and 404. This process is enhanced by inducing an electric field between the upper and
20 lower grids 402 and 404. In the third embodiment, the upper grid 402 is electrically connected to the wall of the processing chamber 210 and thereby grounded. The lower grid 404 is connected to a direct current power source 232 (such as a battery or the like) which places a potential on the lower
25 grid relative to ground. Although two power sources 232a and 232b are shown in Figure 12, it will be readily understood that a single power source may be used for both charged particle filters 240a and 240b. In the third embodiment, the potential is approximately -9 volts, although it will be readily
30 understood by those of ordinary skill in the art that other potentials may be used. Alternatively, for instance, a positive potential could be used. The purpose of applying different potentials to the upper and lower grids is to induce an electric field in the cavity between the two grids.
35 However, it will be understood that the potential difference

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between grids should be limited so as not to induce ionization between the grids. It will be appreciated that the scope of the present invention is broad enough to encompass other methods of inducing charged particle collection (such as by
5 using a magnetic field to direct drifting charged particles in the flowing gas toward conducting vanes or plates where they are collected).

The electric field between the grids attracts charged particles toward one of the grids while allowing a neutral
10 activated species (such as dissociated atoms) to pass through substantially unimpeded. When a negative potential relative to the upper grid is applied to the lower grid, an electric field, E , is induced in the direction of the upper grid as shown in Figure 15A. Thus, negative charged particles entering the
15 filter 240 (e.g. electrons) are attracted to the upper grid 402, while positive charged particles (e.g. ions) are attracted to the lower grid 404. This electric field greatly enhances removal of charged particles from the gas flow by attracting the charged particles to the grids where they collide and are
20 extracted from the gas flow.

Figure 15C illustrates an alternative charged particle filter according to the present invention that uses three grids. In the alternative embodiment of Figure 15C, three grids are used, an upper grid 420, a middle grid 422, and a
25 lower grid 424. The configuration and relation between the upper and middle grids is the same as that described above with reference to Figure 15A. A third lower grid 424 is added below the middle grid 422. The lower grid 424 is separated from the middle grid 422 by approximately 40-50 thousandths of an inch.
30 The lower and middle grids 424, 422 are held apart by a second block of insulating material 426 such as quartz, alumina or mica. An array of holes 428 in the lower grid 424 is offset from an array of holes 430 in the middle grid 422 in the same manner as described previously with reference to Figures 15A
35 and 15B. The array of holes 428 may have the same arrangement

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as an array of holes 432 in the upper grid 420 which is also offset from the array of holes 430 in the middle grid 422. Preferably, there is no line of sight directly through the middle and lower grids 422 and 424. As with the upper and middle grids 420 and 422, the middle and lower grids 422 and 424 block UV radiation and force charged particles and dissociated atoms to travel through a non-linear path. An electric field is induced between the middle and lower grids 422 and 424. This may be accomplished by applying a potential to the lower grid 424 different than that applied to the middle grid 422 to induce an electric field. For the embodiment shown in Figure 15B, a -9 volt potential relative to ground is applied to the middle grid 422 and a ground potential is applied to the lower grid 424. Of course, it will be readily understood that other potentials and other methods could be used to induce an electric field between the middle and lower grids 422 and 424. However, the electric field should be limited so as not to induce ionization between the grids. As described above with reference to Figure 15A, the induced electric field attracts charged particles to the grids which enhances filtering. For the alternative embodiment shown in Figure 15C, positive charged particles (e.g. ions) are attracted to the middle grid 422, while negative charged particles (e.g. electrons) are attracted to the lower grid.

25 The charged particle filters described with reference to Figures 15A, 15B, and 15C greatly reduce the concentration of charged particles that reach wafers 7. With no filter, it is estimated that approximately $.1 \text{ uA/cm}^2$ of charged particle current reaches the wafers 7. With a single grid at ground potential, approximately 10 nA/cm^2 of charged particle current reaches the wafers 7. With two grids having a 9 volt potential difference, less than 0.1 nA/cm^2 (potentially as little as 1 pA/cm^2) of charged particle current reaches the wafers 7. Adding a third grid having a 9 volt potential difference

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relative to the second grid, could reduce charged particle current that reaches the wafers 7 to less than 1 pA/cm².

In a similar manner to that described above, additional grids may be added, although each additional grid slightly
5 reduces the rate of ashing. However, the inductively coupled plasma of the third embodiment provides an abundant amount of dissociated oxygen with reduced modulation of the plasma potential. This allows several grids to be used while still allowing high strip rates of up to approximately 6 microns per
10 minute to be achieved. It will also be noted that by using a plurality of equidistant holes in the filter system of the third embodiment, processing uniformity is maintained in contrast to some conventional barrier or nozzle methods for separating a plasma from a wafer. In addition, the split
15 Faraday shield allows a grid with closely spaced holes with small diameters to be used near the plasma without causing hollow cathode discharge in the holes. Without using the split Faraday shield to reduce the modulation of the plasma potential, it may be difficult to produce a plasma with enough
20 energy to dissociate abundant oxygen atoms without pushing the plasma down to the grid and thereby also inducing hollow cathode discharges in the holes of the grids. Hollow cathode discharges in the filter could cause a dense plasma (with significant charged particles) to form in the filter. This
25 would impair the filter's ability to extract charged particles from the gas flow.

An alternative charged particle filter system 502 is shown in Figure 16. In the filter system 502, two conductive rings 504 and 506 are separated by a block of insulating material 508
30 such as quartz or alumina. The upper ring 504 is grounded while a potential is applied to the lower ring 506. For the embodiment shown in Figure 16, a potential of -30 volts relative to ground is applied to the lower ring 506. This induces an electric field, E_1 , in the plasma reactor chamber
35 and attracts charged particles to the conductive rings 504 and

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506. Of course, other methods for producing an electric field to extract charged particles may be used as well as magnetic filters which sweep the charged particles into conducting metal plates as the flowing gas carries them down from the plasma toward the wafer. It will be noted, however, that the embodiment illustrated in Figure 16 does not block UV radiation that may be produced in the plasma and that may damage an SiO_2 layer or an SiO_2/Si interface on a wafer. In addition, the filter system of Figure 16 is not as effective as the filter systems described with reference to Figures 15A, 15B, and 15C.

The foregoing description is presented to enable any person skilled in the art to make and use the invention. Descriptions of specific designs and dimensions are provided only as examples. Various modifications to the preferred embodiment will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other embodiments and applications without departing from the spirit and scope of the invention. For instance, the gases used and the composition of the plasma can be altered to suit a variety of processes. In particular, the strength of the induction electric field and the pressure of the plasma can be altered to adjust the relative concentrations of dissociated atoms and ions. In this way, a reactor according to the present invention can be adapted for a wide variety of processes. For instance, an argon/oxygen combination at approximately 1 torr could be used for isotropic descum after photolithography. An anisotropic descum could be performed by lowering the pressure below about .1 torr in order to increase the ion density and by putting an RF bias on the block. Selective etch of Si_3N_4 , which may be useful for a pad etch, can be accomplished by using a combination of argon or helium with SF_6 at a pressure from approximately .1 torr to 3 torr. Isotropic silicon etch, which may be useful for contact clean, may be accomplished using argon or helium with H_2 and/or SF_6 or NF_3 at a pressure from approximately .1 torr to 3 torr. Thus,

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the present invention is not intended to be limited to the embodiments shown, but is to be accorded the widest scope consistent with the principles and features disclosed herein.

While the most practical and preferred embodiments of the
5 invention have been disclosed, it will be readily apparent to those skilled in the art that the invention is not limited to the disclosed embodiments but, on the contrary, is intended to cover numerous other modifications and equivalent arrangements which are included within the spirit and scope of the following
10 claims.

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Reference Numerals

1	reactor wall	71	RF voltage signal
2	plasma reactor chamber	72	plasma voltage signal
5 3	gas source	73	peak of plasma voltage signal
4	exhaust system	74	peak of RF voltage signal
5	RF power supply	76	DC component of plasma voltage signal
6	powered electrode	92	electron energy distribution curve for $E/N_0 \sim 2.2 \times 10^{-16} \text{ Vcm}^2$
7	wafer	94	electron energy distribution curve for $E/N_0 \sim 4.4 \times 10^{-16} \text{ Vcm}^2$
10 8	slit valve	96	electron energy distribution curve for $E/N_0 \sim 8.8 \times 10^{-16} \text{ Vcm}^2$
9	plasma body	98	electron energy distribution curve for $E/N_0 \sim 10.9 \times 10^{-16} \text{ Vcm}^2$
10	plasma sheath	202	plasma generation chambers
11	outer tube	204	gas supply system
12	inner tube	206	gas exhaust system
15 13	holes	208	wafer processing chamber
14	gas pipe	210	processing chamber wall
15-16	electrodes	212	aluminum block
17	wafers	213	ceramic support
21-24	elements modeling sheath impedance	214	shut off valve
20 25	low impedance resistance	215	throttle valve
26-28	elements modeling sheath impedance	216	generation chamber wall
31	RF signal	217	top lid of generation chambers
25 32	plasma voltage	218	partition
33	peak of signal 32	220	input pipe
34	peak of signal 31	222	nozzle
35	ground	224	induction coils
30 36	sheath voltage of the powered electrode	226	split Faraday shield
40	base	228	slots in split Faraday shield
41	chamber wall	230	top portion of slots
42	induction coil	231	middle portion of slots
35 43	first RF source	232	DC power source
44	match network	234	bottom portion of slots
45	Faraday shield	240	charged particle filter
46	conductive plates of Faraday shield	302	width of processing chamber
40 47	high voltage end of coil	304	depth of processing chamber
48	gaps between Faraday shield plates	306	outer diameter
49	gas source	308	distance
45 50	plasma reactor chamber	402	upper grid
51	second RF source	404	lower grid
52	powered electrode	406	ring or layer of insulating material
53	grounded conductive plate	408	array of holes in grid 402
54	magnets	410	array of holes in grid 404
50 55	ferromagnetic return plate	420	upper grid
56	conductive coil	422	middle grid
57	DC current source	424	lower grid
58	exhaust port	426	ring or layer of insulating material
59	exhaust system	428	array of holes in grid 424
55 60	aluminum block	430	array of holes in grid 422
61	heating unit	432	array of holes in grid 420
62	plasma	502	filter system
63	inner split Faraday shield		
64	outer split Faraday shield		

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504 upper conductive ring
506 lower conductive ring
508 ring of insulating
material

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WHAT IS CLAIMED IS:

1. A plasma reactor for processing a substrate comprising:
a plasma chamber for producing a plasma having charged
5 particles and an activated neutral species;
a processing chamber containing the substrate; and
a filter disposed between the plasma chamber and the
processing chamber, the filter having a first filter member
forming a plurality of openings for receiving the activated
10 neutral species from the plasma chamber and a second filter
member forming a plurality of openings for providing the
activated neutral species to the processing chamber;
wherein the filter forms a flow path from the plasma
chamber to the processing chamber such that the activated
15 neutral species flows from the plasma chamber to the substrate
for processing, and the first and second filter members each
comprise a conductive material for filtering the charged
particles from the flow path.
- 20 2. The plasma reactor of claim 1, wherein the second
filter member is closely spaced to the first filter member
forming a small gap between the first and second filter members
for trapping and filtering the charged particles.
- 25 3. The plasma reactor of claim 1, wherein the second
filter member forms a surface substantially parallel to the
substrate.
- 30 4. The plasma reactor of claim 1, wherein the openings in
the second filter member are offset from the openings in the
first filter member such that ultraviolet radiation is
substantially prevented from radiating from the plasma chamber
to the processing chamber through the filter.
- 35 5. The plasma reactor of claim 1, wherein the flow path
is nonlinear.

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6. The plasma reactor of claim 1, wherein a potential of the plasma relative to the filter is maintained at a level such that a plasma discharge within the filter is substantially prevented.

5

7. The plasma reactor of claim 1, wherein said first and second filter members are spaced apart by an insulating material.

10 8. The plasma reactor of claim 1, wherein the openings in the second filter member are offset from the openings in the first filter member; and

the distance between the first and second filter members and the size of the openings in the first and second filter
15 members are selected such that substantially all paths through the filter are non-linear.

9. The plasma reactor of claim 1, wherein each of the openings in the first filter member has a diameter and is
20 spaced from adjacent openings in the first filter member by a distance less than the diameter.

10. The plasma reactor of claim 1, wherein each of the openings in the second filter member has a diameter and is
25 spaced from adjacent openings in the second filter member by a distance less than the diameter.

11. The plasma reactor of claim 9, wherein each respective opening in the second filter member has a diameter
30 and is spaced from adjacent openings in the second filter member by a distance less than the diameter of the respective opening in the second filter member.

12. The plasma reactor of claim 1, wherein the first and
35 second filter members are spaced apart by a distance, and the

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first filter member has a thickness greater than the distance between the first and second filter members.

13. The plasma reactor of claim 12, wherein the second
5 filter member has a thickness greater than the distance between the first and second filter members.

14. The plasma reactor of claim 9, wherein the first and second filter members are spaced apart by a short distance, and
10 the diameter of each of the openings in the second filter member is greater than the short distance.

15. The plasma reactor of claim 1, further comprising a power source coupled to the filter such that an electric field
15 is induced between the first and second filter members to enhance the filtration of the charged particles from the flow path.

16. The plasma reactor of claim 2, further comprising a
20 power source for inducing an electric field across the small gap such that the charged particles are deflected from the flow path.

17. The plasma reactor of claim 11, further comprising a
25 power source coupled to the filter such that an electric field is induced between the first and second filter members to enhance the filtration of the charged particles from the flow path.

30 18. The plasma reactor of claim 12, further comprising a power source coupled to the filter such that an electric field is induced between the first and second filter members to enhance the filtration of the charged particles from the flow path.

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19. The plasma reactor of claim 1, further comprising a third filter member disposed between the first filter member and the second filter member, the third filter member forming a plurality of openings such that the activated neutral species
5 may pass through the third filter member.

20. The plasma reactor of claim 19, wherein the third filter member comprises a conductive material for filtering charged particles from the flow path.
10

21. The plasma reactor of claim 19, further comprising a power source coupled to the filter such that an electric field is induced between the first and third filter members to enhance the filtration of the charged particles from the flow
15 path.

22. The plasma reactor of claim 19, further comprising a power source coupled to the filter such that an electric field is induced between the second and third filter members to
20 enhance the filtration of the charged particles from the flow path.

23. The plasma reactor of claim 21, wherein a second electric field is induced between the third and second filter
25 members to enhance the filtration of the charged particles from the flow path.

24. A plasma reactor for processing a substrate comprising:
30 a plasma chamber for producing a plasma having charged particles and an activated neutral species;
a processing chamber containing the substrate; and
a filter disposed between the plasma chamber and the processing chamber, the filter comprising a plurality of
35 adjacent grids including a first grid forming an array of

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closely spaced openings and a second grid forming an array of closely spaced openings offset from the array of closely spaced openings in the first grid;

wherein a flow path is defined between the plasma chamber
5 and the processing chamber and the first and second grids
comprise a conductive material for filtering charged particles
from the flow path.

25. The plasma reactor of claim 24, wherein each grid is
10 closely spaced to an adjacent grid such that a small gap is
formed for trapping and filtering the charged particles.

26. The plasma reactor of claim 24 wherein the first and
second grids are spaced apart by a short distance, and at least
15 one of the grids of the filter has a thickness greater than the
short distance.

27. The plasma reactor of claim 24, wherein the grids are
substantially parallel to the substrate.

20

28. The plasma reactor of claim 27, further comprising a
first source of radio frequency power; and

an induction coil adjacent to the plasma chamber, the
induction coil coupled to the first source of radio frequency
25 power to inductively couple power into the plasma from the
first source of radio frequency power;

wherein the induction coil produces an induction electric
field in the plasma chamber substantially parallel to the grids
and the substrate.

30

29. The plasma reactor of claim 28, further comprising a
split Faraday shield disposed between the induction coil and
the plasma chamber.

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30. The plasma reactor of claim 25, further comprising a third grid forming an array of closely spaced openings offset from the array of closely spaced openings in the second grid.

5 31. The plasma reactor of claim 25, further comprising a power source wherein the power source induces an electric field within the filter such that charged particles are attracted to at least one of the grids of the filter.

10 32. A plasma reactor for processing a substrate comprising:

 a chamber including a plasma producing region for producing a plasma having charged particles and an activated neutral species,

15 the substrate positioned a distance from the plasma producing region,

 a flow path being defined between the plasma producing region and the substrate, the flow path being substantially perpendicular to a surface of the substrate;

20 a first source of radio frequency power;

 an induction coil adjacent to the plasma producing region of the chamber, the induction coil coupled to the first source of radio frequency power to inductively couple power into the plasma from the first source of radio frequency power;

25 a conductive material adjacent to the flow path between the plasma producing region and the substrate; and

 a second power source for inducing an electric field within the chamber such that charged particles are deflected from the flow path to the conductive material for filtration.

30

33. The plasma reactor of claim 32, wherein the conductive material comprises a ring substantially surrounding the flow path.

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34. The plasma reactor of claim 32, wherein the conductive material comprises a grid substantially transverse to the flow path.

5 35. The plasma reactor of claim 32, further comprising a split Faraday shield disposed between the induction coil and the plasma producing region of the chamber.

36. A plasma reactor for processing a substrate
10 comprising:

 a chamber including a plasma producing region for producing a plasma having charged particles and an activated neutral species;

 the substrate being positioned a distance from the plasma
15 producing region;

 a flow path being defined between the plasma producing region and the substrate;

 a first source of radio frequency power;

 an induction coil adjacent to the plasma producing region
20 of the chamber, the induction coil coupled to the first source of radio frequency power to inductively couple power into the plasma from the first source of radio frequency power;

 a charged particle filter disposed between the plasma producing region and the substrate transverse to the flow path;
25 and

 a split Faraday shield between the induction coil and the plasma, the split Faraday shield forming at least one substantially nonconductive gap such that the induction coil inductively couples power into the plasma from the first radio
30 frequency power source;

 wherein the split Faraday shield substantially reduces capacitive coupling between the induction coil and the plasma thereby reducing modulation of the plasma potential relative to the potential of the charged particle filter.

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37. The plasma reactor of claim 36, wherein the charged particle filter comprises a conductive material.

38. . The plasma reactor of claim 36, wherein the filter is
5 substantially planar and the induction coil produces an induction electric field in the chamber that is substantially parallel to the filter.

39. The plasma reactor of claim 38, wherein the filter
10 comprises a conductive material.

40. The plasma reactor of claim 37, wherein the filter forms at least one hollow region and the split Faraday shield prevents hollow cathode discharge from occurring in the hollow
15 region of the filter.

41. The plasma reactor of claim 37, wherein the charged particle filter comprises a plurality of adjacent grids including a first grid forming an array of closely spaced
20 openings and a second grid forming an array of closely spaced openings offset from the array of closely spaced openings in the first grid.

42. The plasma reactor of claim 41, wherein the first and
25 second grids are spaced apart by a short distance, and at least one of the grids of the filter has a thickness greater than the short distance.

43. The plasma reactor of claim 37, further comprising a
30 second power source coupled to the filter such that charged particles are attracted to the filter.

44. The plasma reactor of claim 41, further comprising a second power source wherein the second power source induces an

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electric field within the filter such that charged particles are attracted to the grids of the filter.

45. A plasma reactor for processing a substrate
5 comprising:

a plasma chamber for producing a plasma;
a first source of radio frequency power;
an induction coil adjacent the plasma and coupled to the
first source of radio frequency power to inductively couple
10 power into the plasma from the first radio frequency power
source;

a split Faraday shield between the induction coil and the
plasma, the split Faraday shield forming at least one
substantially nonconductive gap such that the induction coil
15 inductively couples power into the plasma from the first radio
frequency power source; and

a support for the substrate positioned such that the
substrate is exposed to products of the plasma for processing;
wherein a capacitance and an inductance are provided
20 between the support and a ground potential and the capacitance
and the inductance are selected to provide a high impedance for
a given frequency of the first source of radio frequency power
during processing.

25 46. The plasma reactor of claim 45, wherein the
capacitance and the inductance are provided by a parallel LC
circuit disposed between the support and the ground potential.

47. The plasma reactor of claim 45, further comprising a
30 charged particle filter disposed between the plasma and the
substrate.

48. A plasma reactor for processing a substrate
comprising:
35 a plasma chamber for producing a plasma;

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a first source of radio frequency power;

an induction coil adjacent the plasma and coupled to the first source of radio frequency power to inductively couple power into the plasma from the first radio frequency power

5 source;

a split Faraday shield between the induction coil and the plasma, the split Faraday shield forming at least one substantially nonconductive gap such that the induction coil inductively couples power into the plasma from the first radio

10 frequency power source; and

a support for the substrate positioned such that the substrate is exposed to products of the plasma for processing;

wherein a variable impedance is provided between the split Faraday shield and a ground potential and the variable

15 impedance is selected such that the impedance is very low for a given frequency of the first source of radio frequency power during processing.

49. The plasma reactor of claim 48, wherein the variable
20 impedance is selected such that the impedance is high for the given frequency during ignition of the plasma reactor.

50. The plasma reactor of claim 48, wherein the variable impedance is provided by a series LC circuit.

25

51. The plasma reactor of claim 50, wherein the series LC circuit includes a variable capacitor.

52. The plasma reactor of claim 48, further comprising a
30 charged particle filter disposed between the plasma and the substrate.

53. The plasma reactor of claim 52, wherein the charged particle filter comprises a conductive material.

35

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54. The plasma reactor of claim 53, wherein the charged particle filter comprises a plurality of adjacent grids including a first grid forming an array of closely spaced openings and a second grid forming an array of closely spaced openings offset from the array of closely spaced openings in the first grid.

55. A method for removing photoresist from a surface of a substrate, the method comprising the steps of:

10 forming a plasma in a chamber, the plasma containing an activated neutral species and charged particles;

positioning the substrate a distance from the plasma;

inducing a flow of the activated neutral species from the plasma substantially perpendicularly toward the surface of the

15 substrate;

deflecting the charged particles from the flow to a conductive collecting vane positioned between the plasma and the substrate; and

exposing the surface of the substrate to the activated

20 neutral species to remove the photoresist.

56. The method of claim 55, wherein the charged particles are deflected using an electric field transverse to the flow path.

25

57. The method of claim 55, wherein the charged particles are deflected using a magnetic field.

58. The method of claim 55, wherein the conductive

30 collecting vane is a ring electrode substantially surrounding the flow.

59. The method of claim 55, wherein the conductive collecting vane forms a first plurality of closely spaced

35 openings and the step of inducing a flow further comprises the

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step of causing the activated neutral species to flow through the first plurality of closely spaced openings.

60. The method of claim 59, wherein the conductive
5 collecting vane forms a second plurality of closely spaced openings offset from the first plurality of closely spaced openings and the step of inducing a flow further comprises the step of causing the activated neutral species to flow through the second plurality of closely spaced openings.

10

61. The method of claim 55, wherein the step of forming a plasma further comprises the step of inducing an electric field in the chamber substantially parallel to the substrate.

15 62. The method of claim 61, wherein the conductive collecting vane is substantially planar and is substantially parallel to the substrate.

63. The method of claim 55, wherein the step of forming a
20 plasma further comprises the steps of:

inductively coupling power from an induction coil into the plasma; and

shielding the chamber to substantially reduce capacitive coupling of power from the induction coil into the plasma.

25

64. A method for removing photoresist from a surface of a substrate, the method comprising the steps of:

forming a plasma in a chamber, the plasma containing an activated neutral species and charged particles;

30 positioning the substrate a distance from the plasma;

positioning a conductive charged particle filter between the plasma and the substrate, the charged particle filter forming a first plurality of holes for receiving the activated neutral species from the plasma and a second plurality of holes

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offset from the first plurality of holes for providing the activated neutral species to the substrate;

inducing a flow of the activated neutral species from the plasma to the substrate through the charged particle filter;

5 and

exposing the surface of the substrate to the activated neutral species to remove the photoresist.

65. The method of claim 64, wherein the step of exposing
10 the substrate to the activated neutral species further comprises the step of dispensing the activated neutral species through the second plurality of openings substantially uniformly across the surface of the substrate.

15 66. The method of claim 64, further comprising the step of maintaining at least two portions of the filter at different potentials.

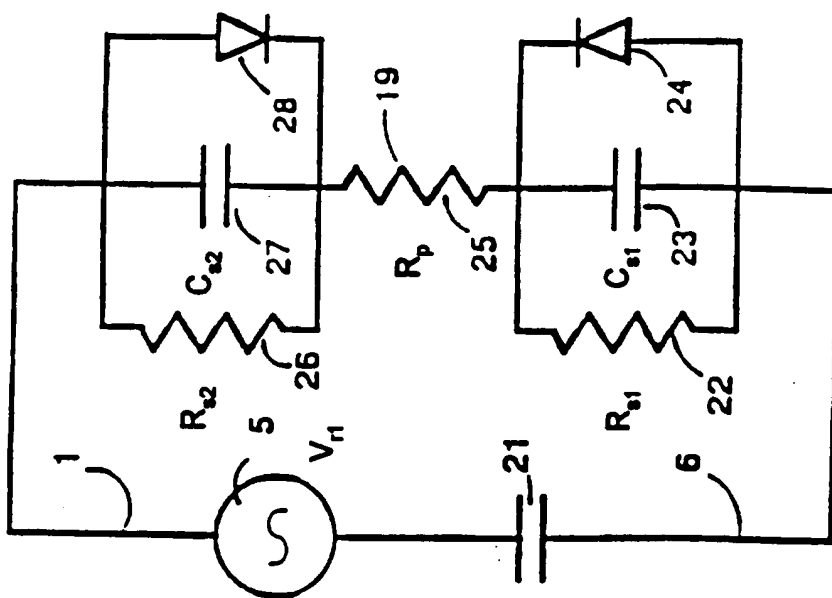


Figure 3 (Prior Art)

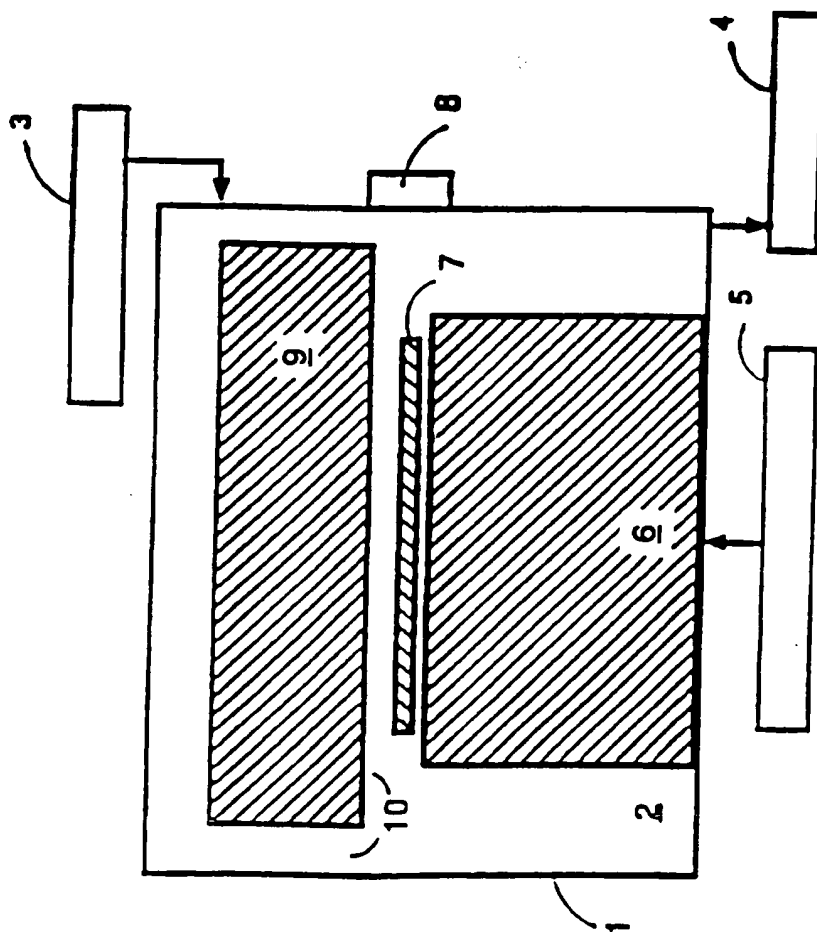


Figure 1 (Prior Art)

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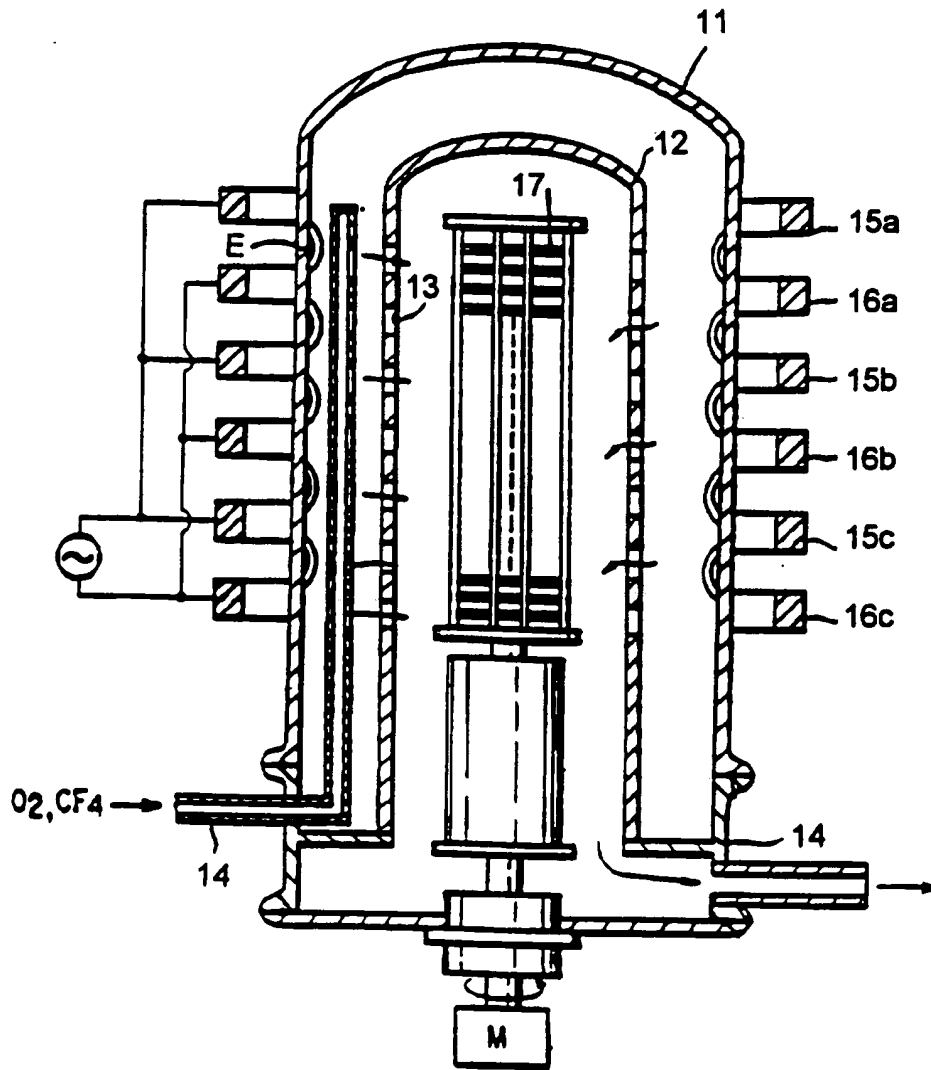


Figure 2
(Prior Art)

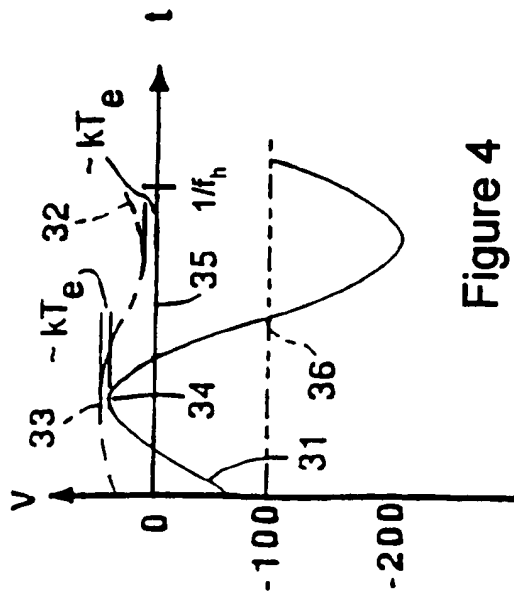


Figure 4

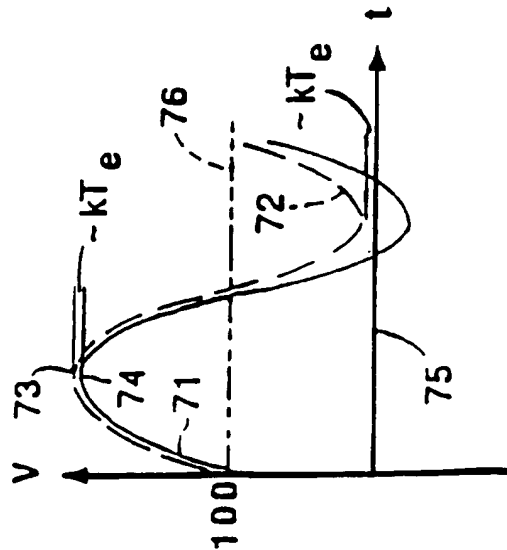


Figure 8

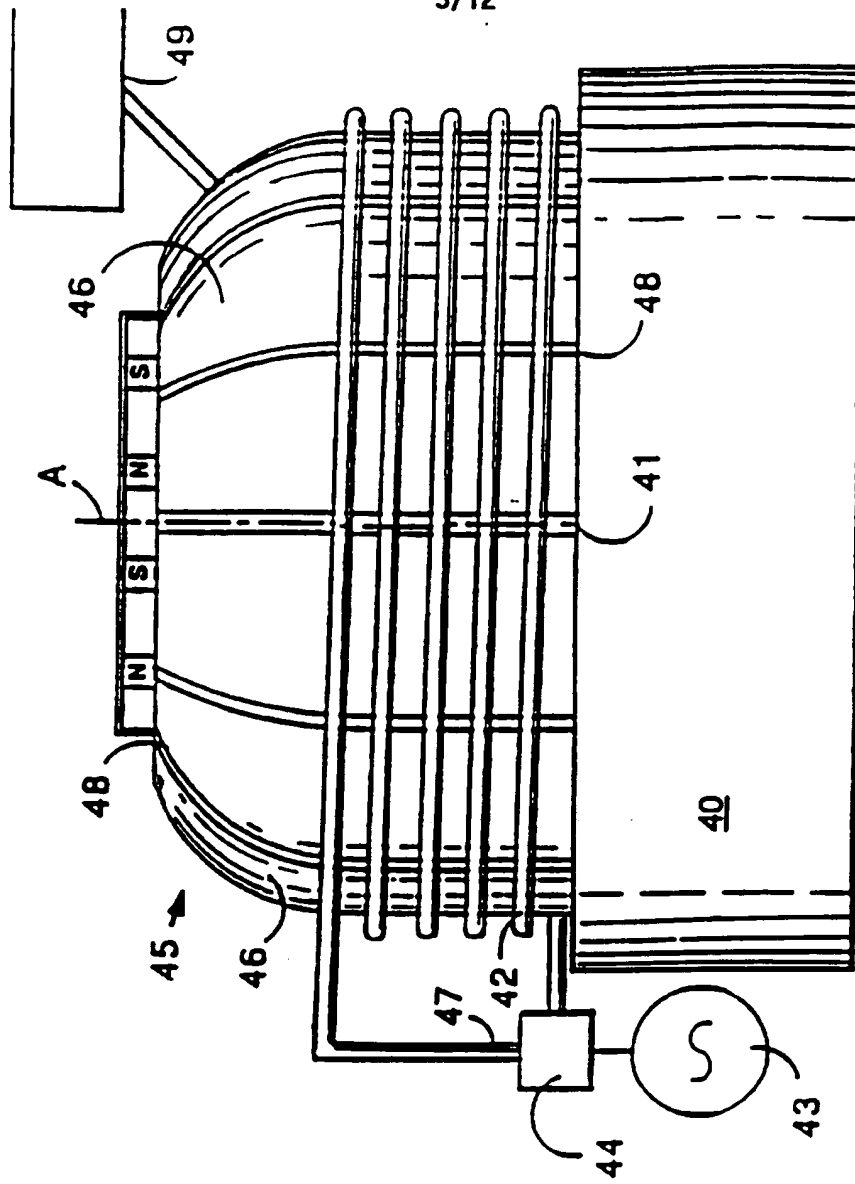


Figure 5

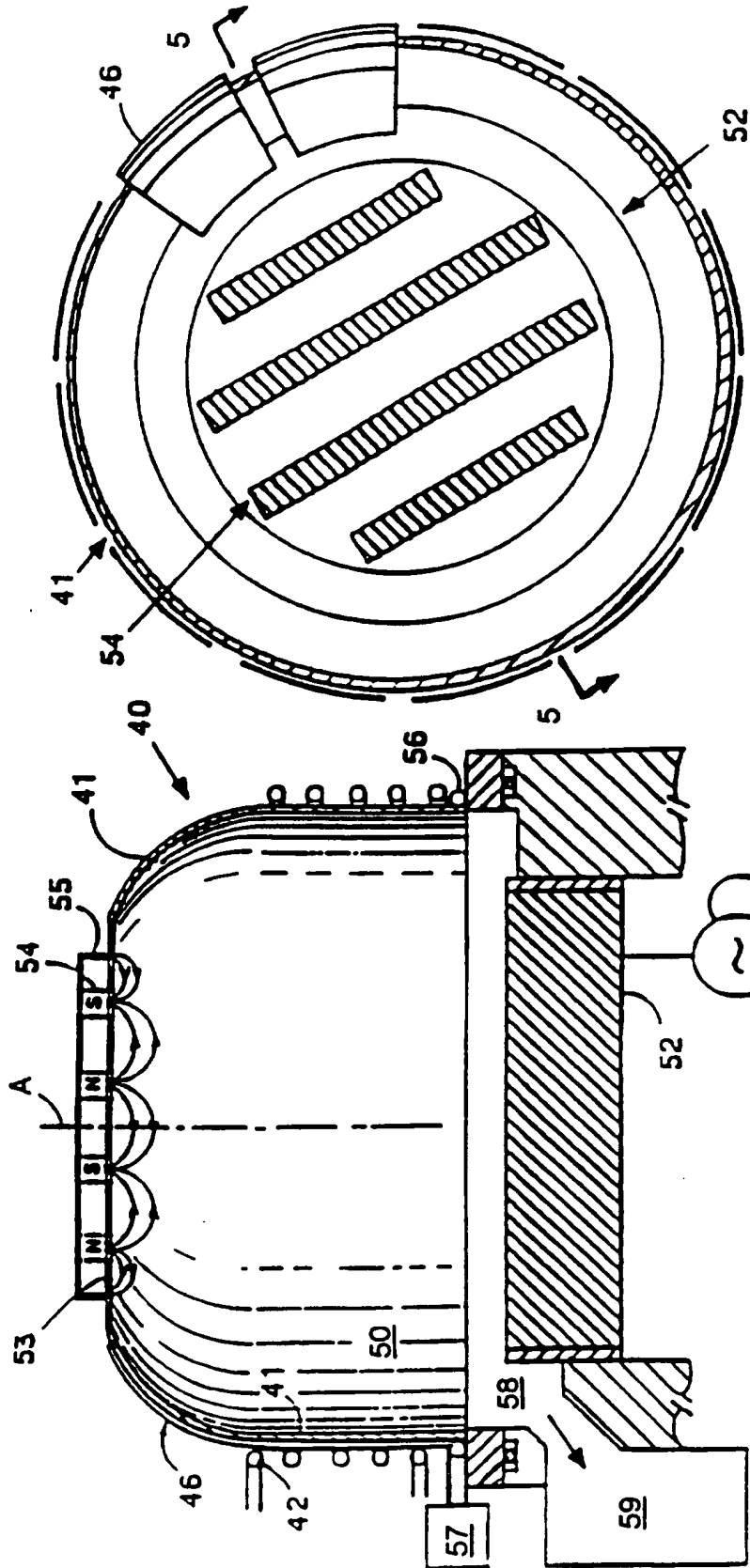


Figure 7

Figure 6

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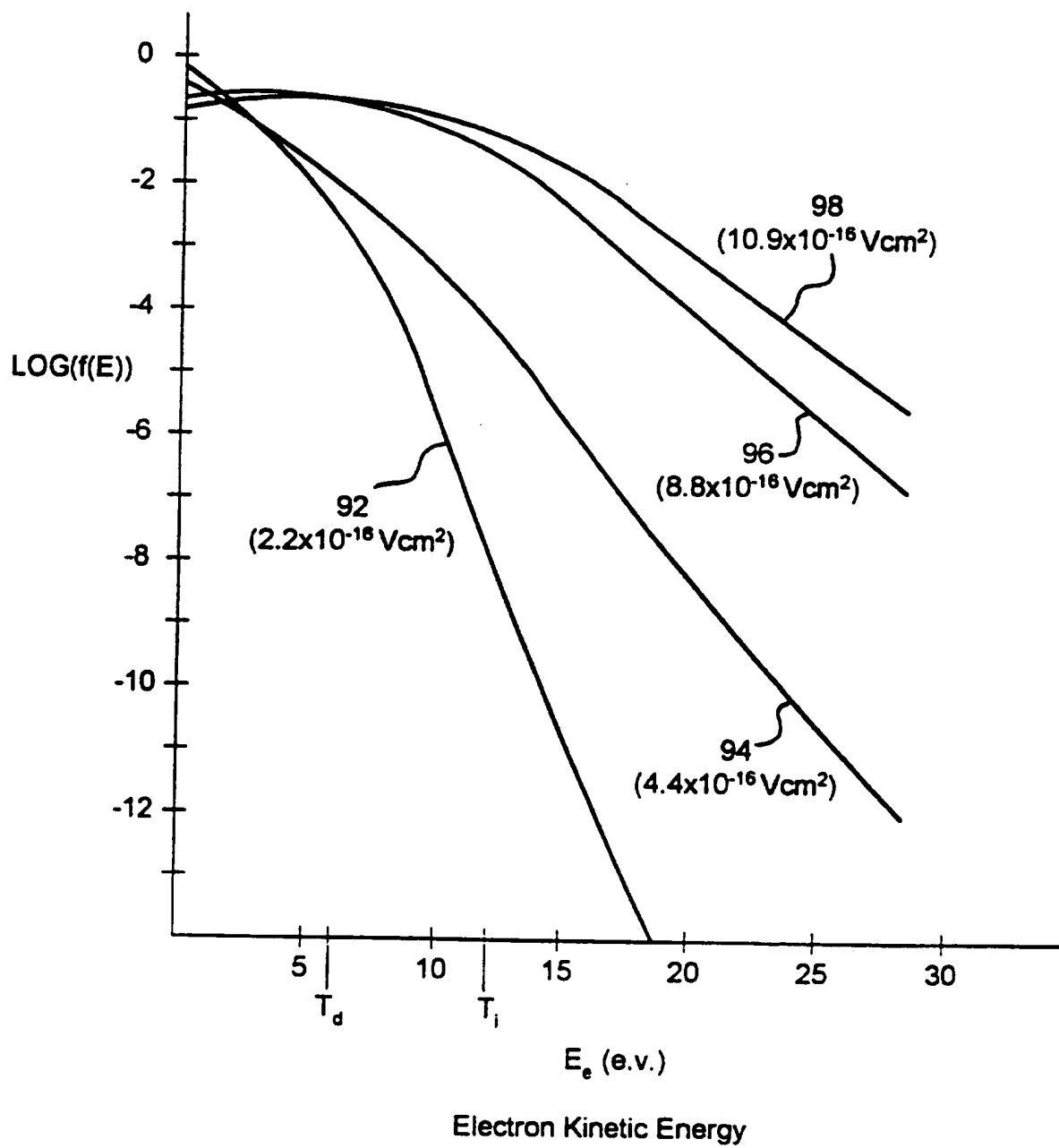


Fig. 9

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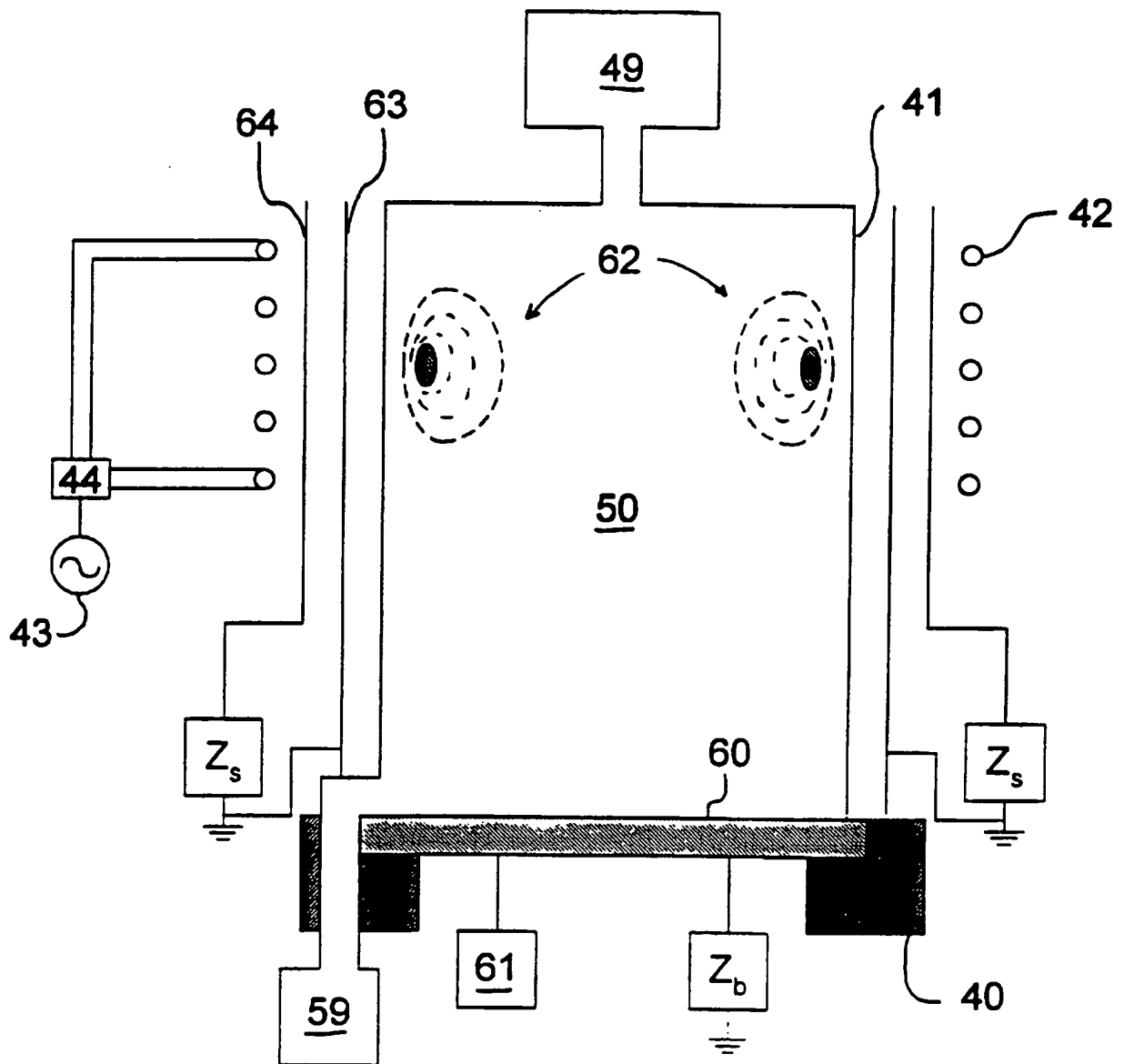


Figure 10

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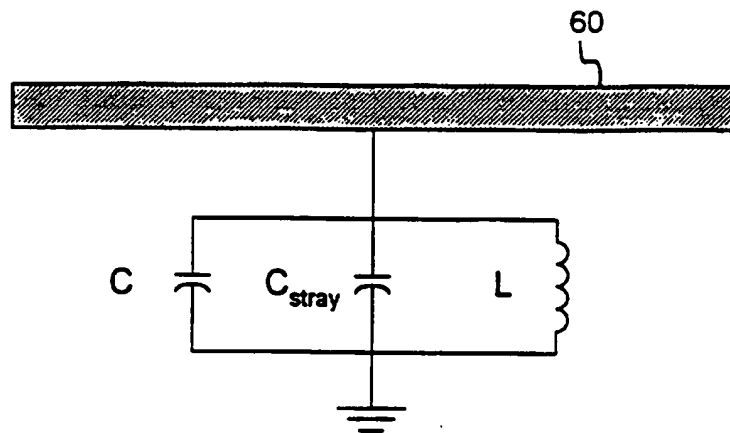


Figure 11A

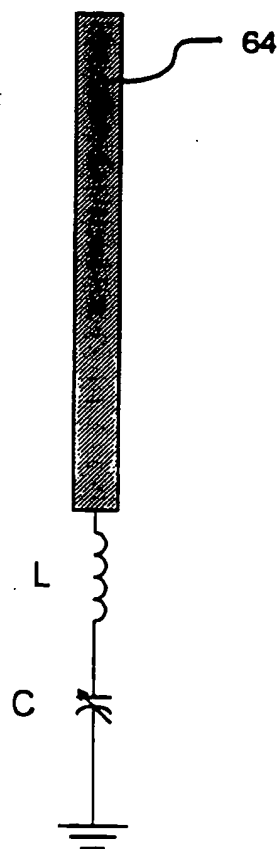


Figure 11B

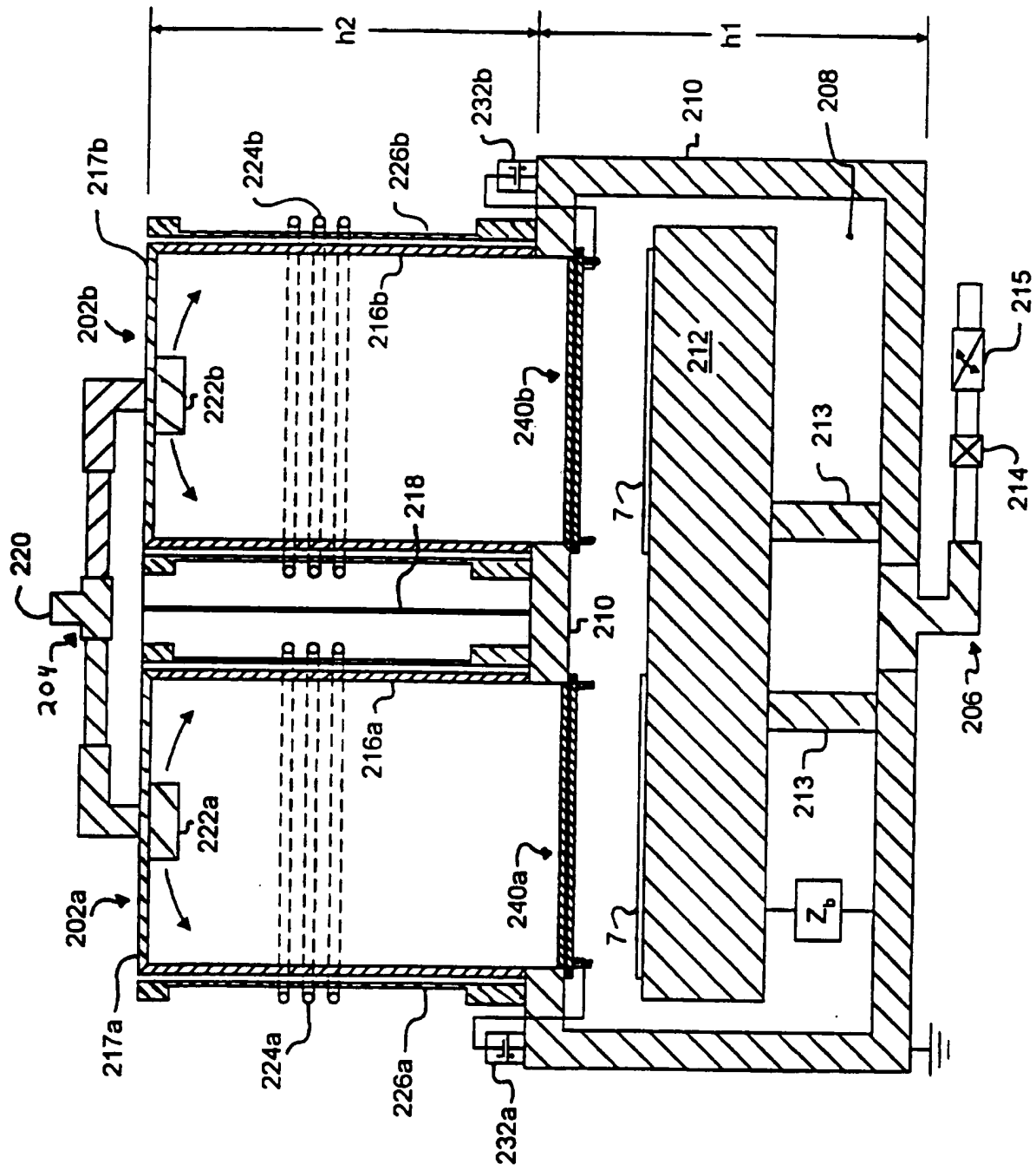


Fig. 12

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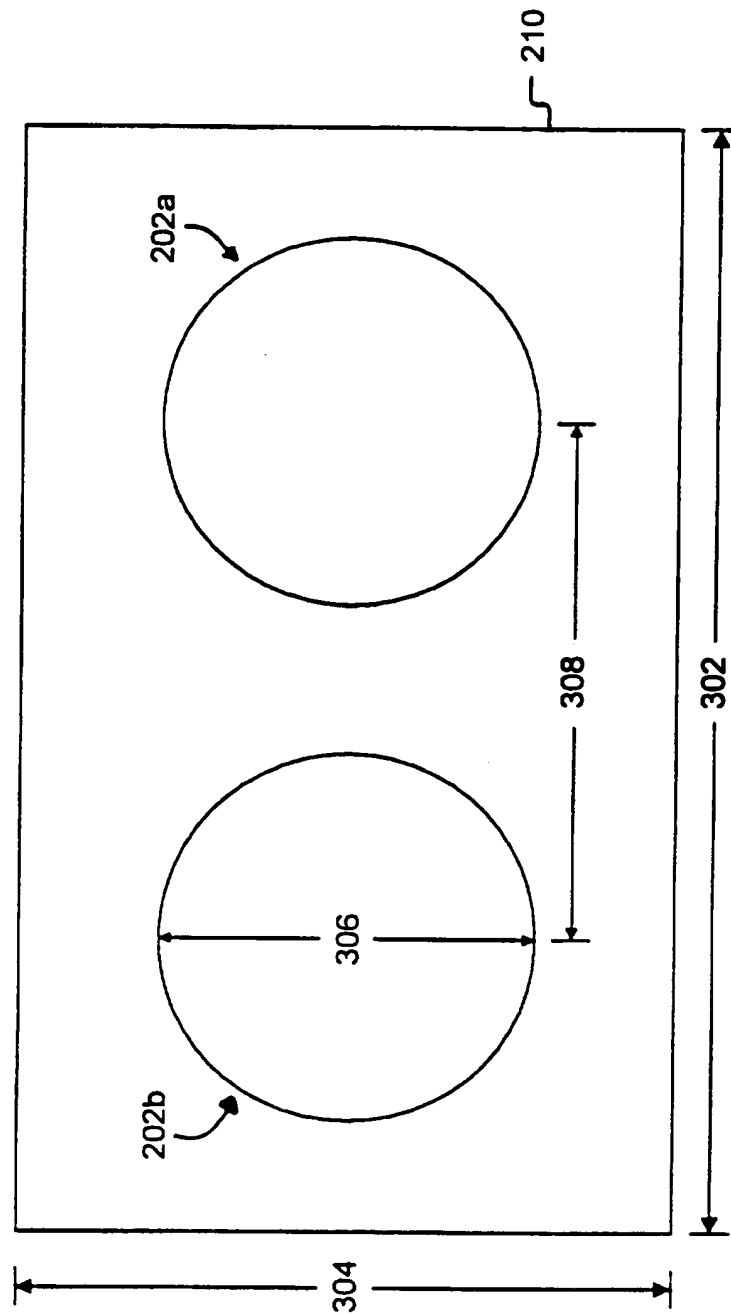


Fig. 13

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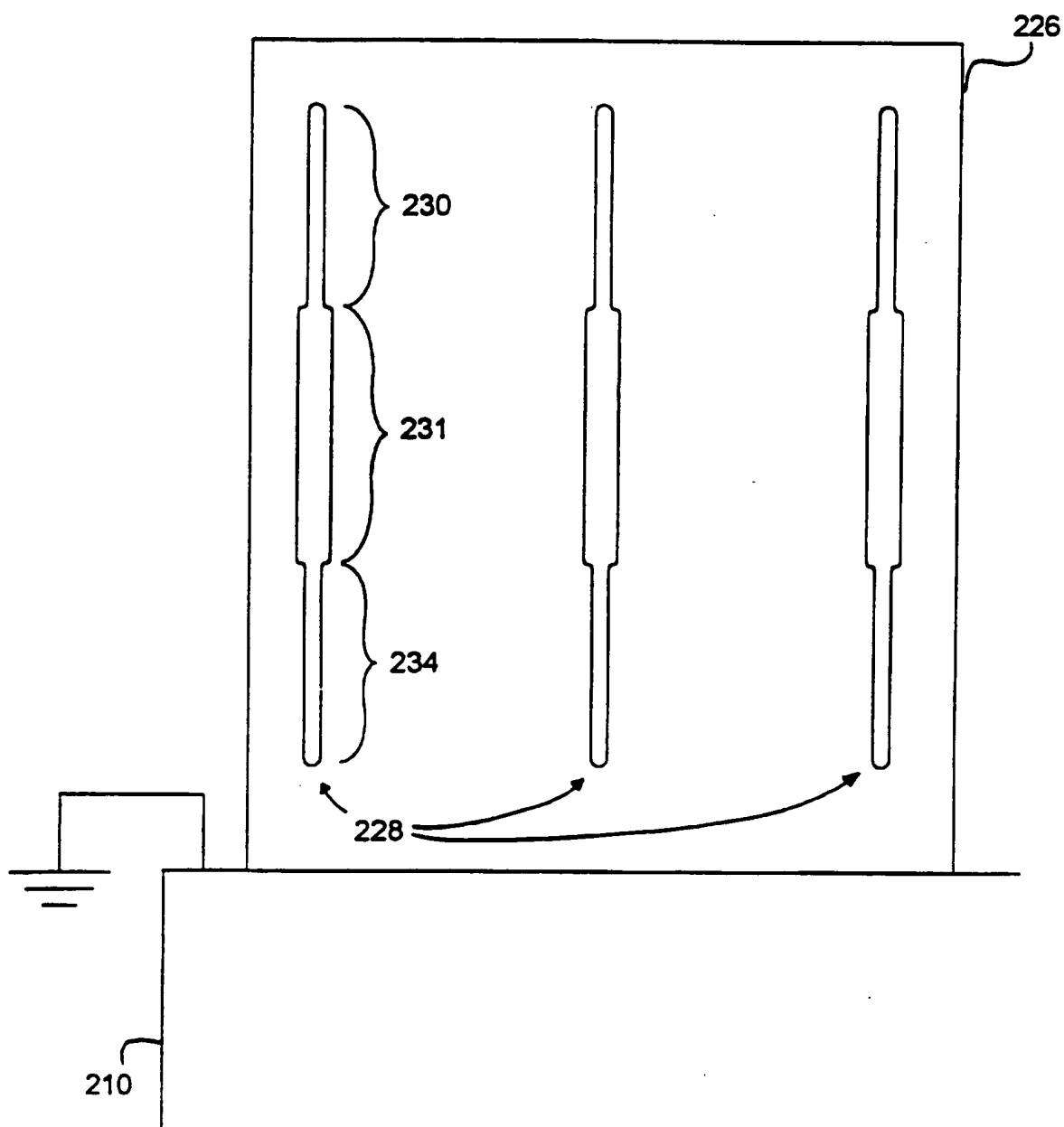


Fig. 14

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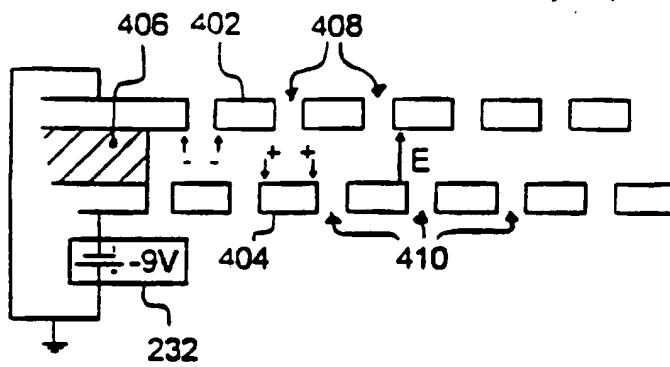


Fig. 15A

Fig. 15B

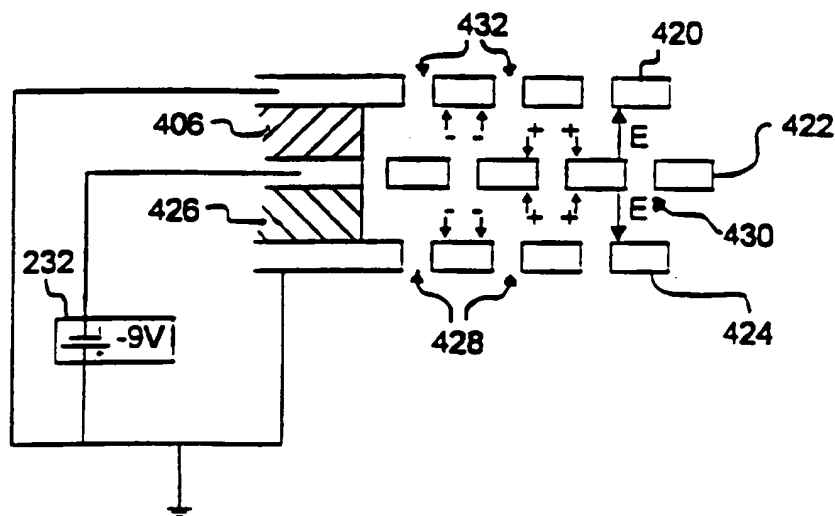
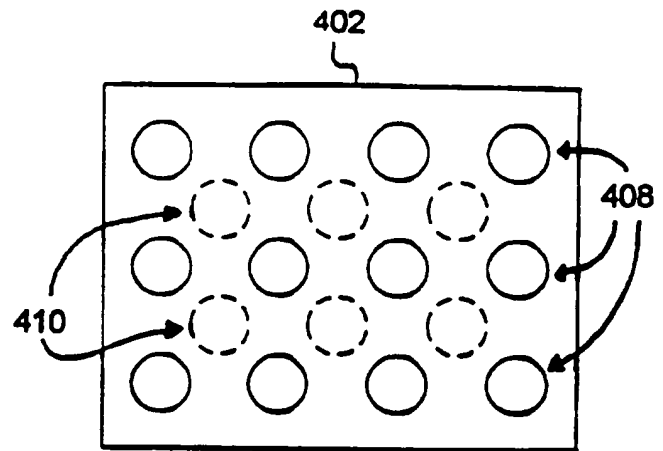


Fig. 15C

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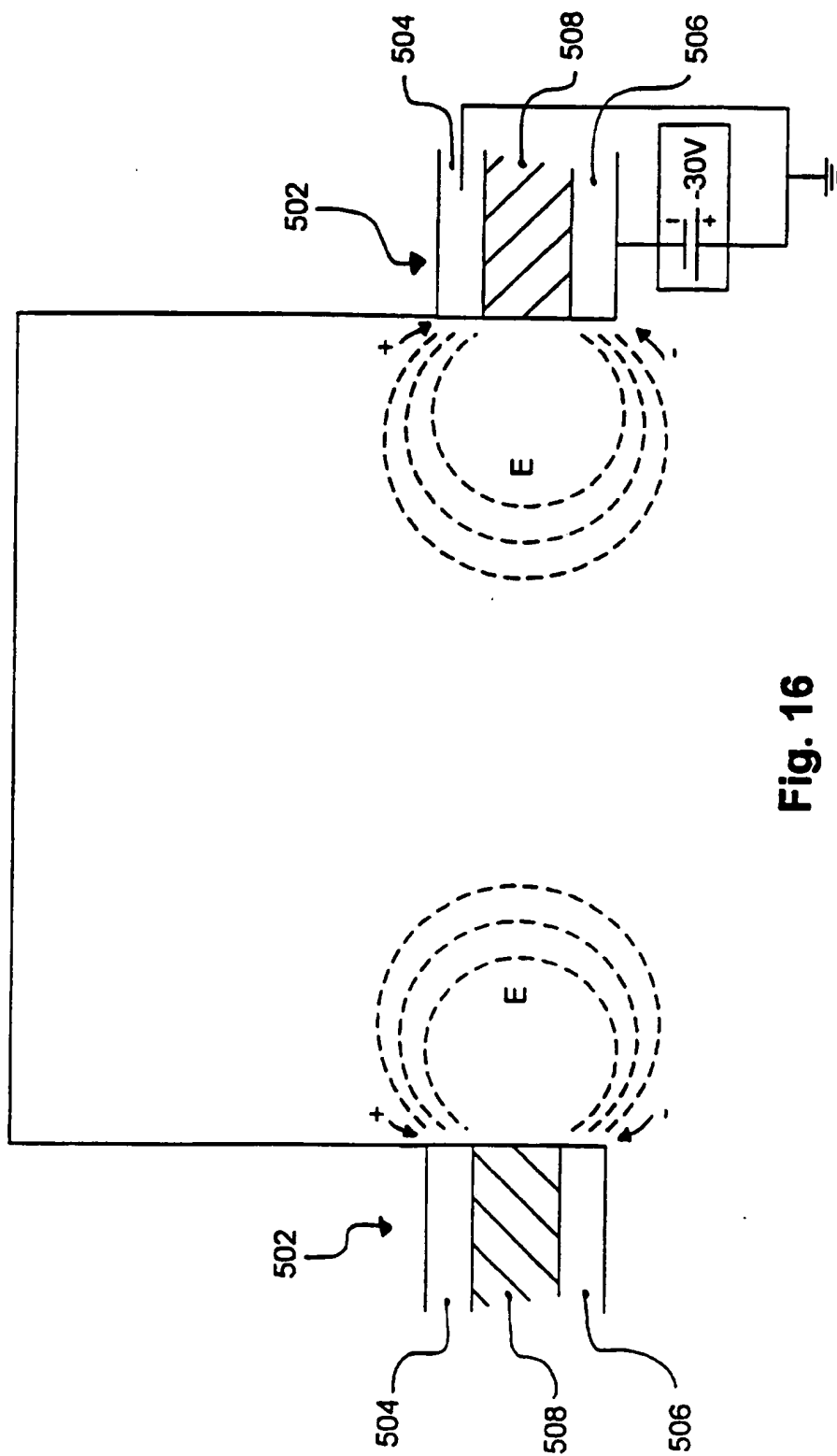


Fig. 16

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 95/14614

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H01J37/32

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 H01J H05H

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 018 no. 336 (E-1568) ,24 June 1994 & JP,A,06 084837 (MITSUBISHI ELECTRIC CORP) 25 March 1994, see abstract	1-5,8, 24,25,27
Y		19,20, 28,29, 32, 34-41, 43, 45-47, 55,56, 59-64,66

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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

3 April 1996

Date of mailing of the international search report

19.04.96

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Fax (+ 31-70) 340-3016

Authorized officer

Capostagno, E

INTERNATIONAL SEARCH REPORT

International Application No.

PCT/US 95/14614

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO,A,91 10341 (SAVAS STEPHEN E) 11 July 1991 cited in the application	28,29, 32, 34-41, 43, 45-47, 55,56, 59-64,66
A	see page 5, line 28 - page 6, line 8 see page 12, line 9 - page 13, line 5 see page 14, line 23 - line 31 see figures 4,5	48,61,63
Y	EP,A,0 443 154 (HITACHI LTD) 28 August 1991 see column 7, line 14 - column 8, line 40 see column 12, line 57 - column 13, line 23	19,20
A	see claims 15,29; figures 3,10	14-18, 21-23, 30,41,54
Y	DE,A,38 01 309 (LEYBOLD AG) 27 July 1989 see column 2, line 64 - column 3, line 15 see figure 1	45-47
Y	EP,A,0 131 433 (FUJITSU LTD) 16 January 1985 see page 5, line 34 - page 6, line 26 see page 13, line 25 - line 32	55,56, 59-64,66
A	EP,A,0 132 015 (UNIV TOKYO) 23 January 1985 see page 7, line 22 - page 8, line 6 see page 9, line 26 - page 11, line 3 see page 12, line 26 - page 13, line 10 see page 13, line 23 - page 14, line 6 see figure 2	6,7, 9-18,37, 39, 42-44, 53,66
A	US,A,5 234 529 (JOHNSON WAYNE L) 10 August 1993 see the whole document	28,29, 32,35, 36,45, 48,61,63

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 95/14614

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EP-A-0131433	16-01-85	JP-C- 1799611 JP-B- 5005167 JP-A- 60016424 US-A- 4512868	12-11-93 21-01-93 28-01-85 23-04-85
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US-A-5234529	10-08-93	NONE	